SYNOPSYS®
Silicon to Software™

Logic Analyzer Interface Board
Part of the Synopsys HAPS FPGA-Based Prototyping Solution

Overview

Synopsys offers a broad portfolio of off-the-shelf systems, daughter boards and accessories that allow software development, HW/SW integration and system validation teams to quickly assemble a prototyping system with real-world interfaces. The portfolio supports a wide variety of memory modules, interconnects, adapters and high-speed interfaces such as PCIe, SATA, and USB. All boards conform to the HapsTrak™ standard, which guarantees compatibility with previous and future generation HAPS systems and daughter boards.

For more information on Synopsys' FPGA-based prototyping solutions visit www.synopsys.com/ fpgabasedprototyping.

HAPS Logic Analyzer Interface Board

The HAPS® Logic Analyzer Interface (LAI_HT3) board allows probing of dedicated signals with standard Logic Analyzers.

The LAI_HT3 is a pass-through board for all power and I/O signals passing through a single HapsTrak 3 connector on a HAPS system. All signals and clocks are connected to two positions where Soft Touch (Pro) or D-Max probes can be placed. In total there are 62 signals that may be probed. The LAI_HT3 can be used for in-system debugging of the interface between a HAPS system and a HapsTrak 3 daughter board, and probing internal FPGA signals.

Features

▶ Support for Agilent Pro Series Soft Touch and Tektronix P6900 Series Probes
▶ Two probe pod locations
▶ Up to 62 probe signals
▶ Support for single-ended or differential probing
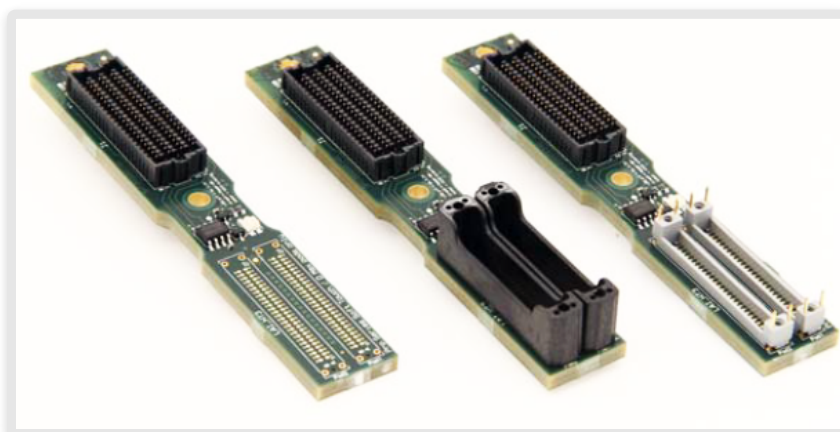▶ HapsTrak 3 connector for in-system debugging with a HapsTrak 3 daughter board

Figure 1: Examples of different retention modules