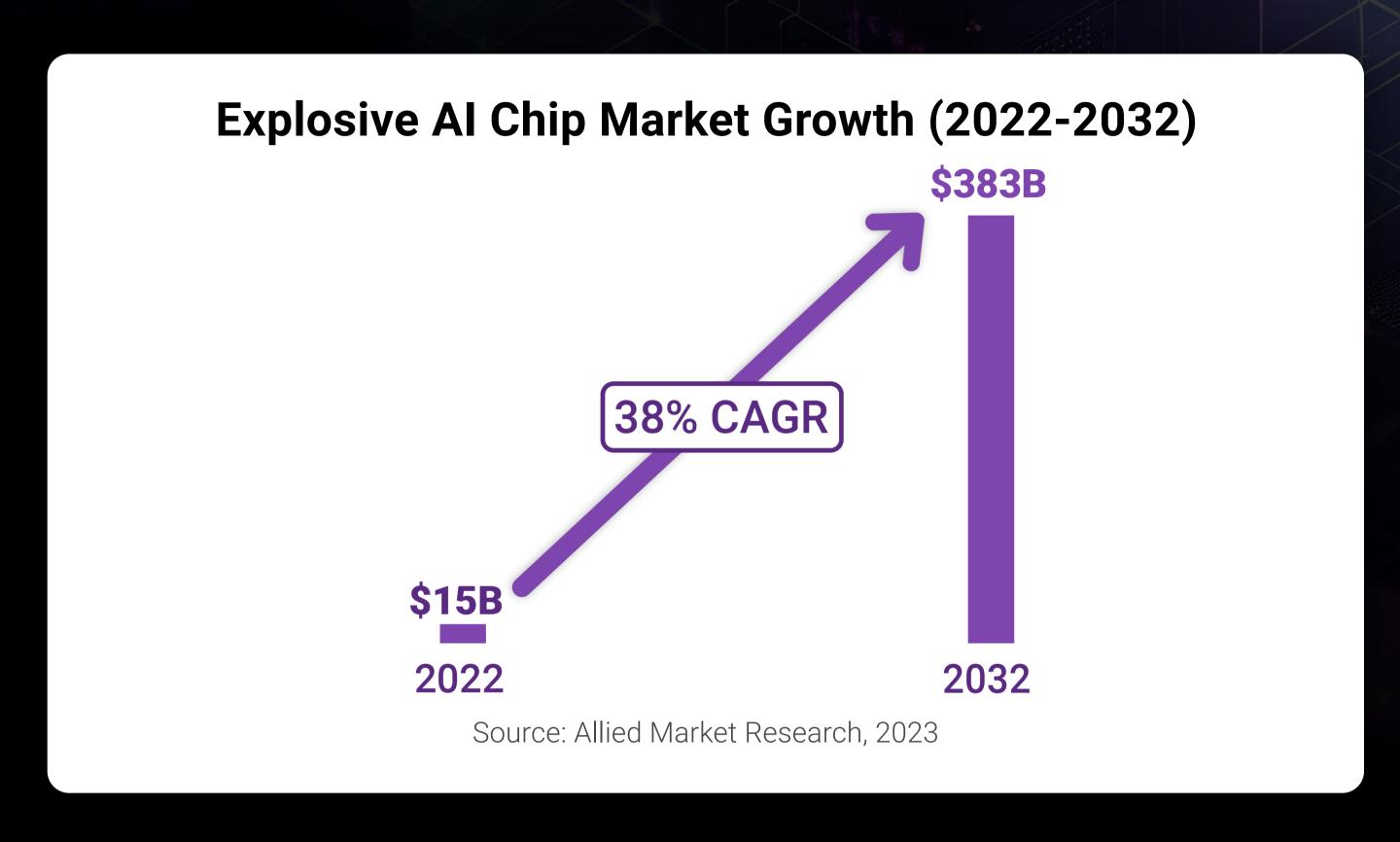
SYNOPSYS®

Navigating Al Chip Development

Opportunity



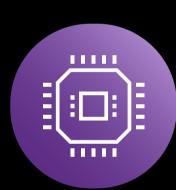
Market Opportunity

\$383B

Global AI Chip Market Projected by 2032

CAGR Growth Rate for AI Chips Through 2032

Challenges



58% of ASICs require a respin

Logic and functional flaws are the leading cause resulting in costly delays.

Source: Synopsys Global User Survey, 2024



Software complexity skyrocketing

Early software development and hardwareassisted verification are now essential.



Chip development time cut in half

Al chip roadmaps have shifted from 18-24 months to 12 month release cycles.



Electricity consumption will reach 1000 TWh in 2026

This demand is roughly equivalent to the electricity consumption of Japan.

Source: IEA Electricity 2024 - Analysis and forecast to 2026

Synopsys Solution

