

Optimization of mask error correction quality for low-k₁ curvilinear mask lithography

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a) Samsung Inc. Korea

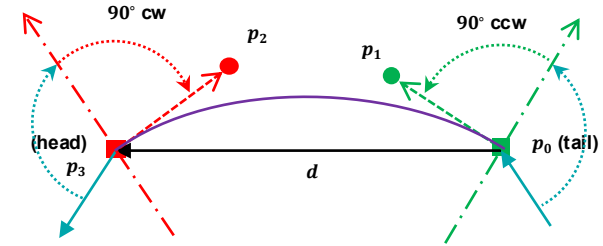
b) Synopsys

Outline

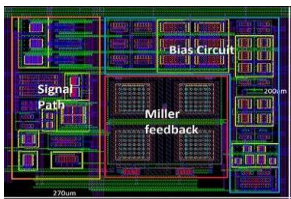
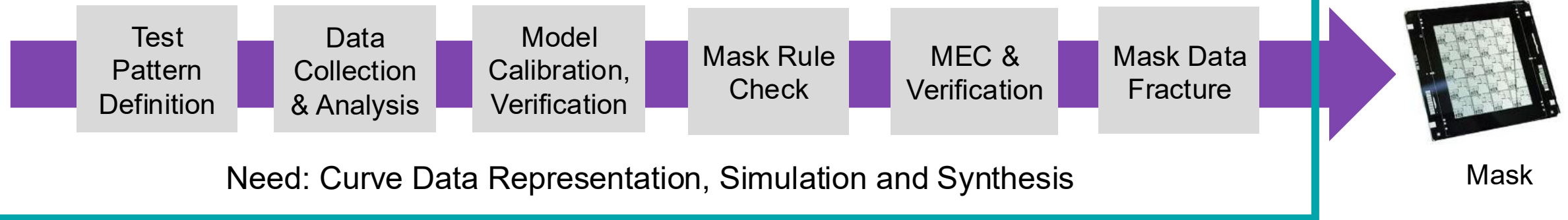
- Introduction to curvilinear mask lithography flow
- Curvilinear Mask process data and modeling
- Curvilinear Mask error correction (MEC) and verification
- Curvilinear Mask correction file size implications
- Summary

Full curvilinear mask ecosystem requirements

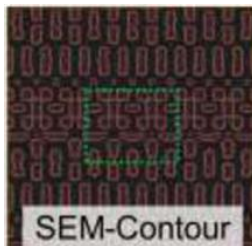
- Users need a full flow of curve/Multigon-ready solutions to quickly and accurately complete steps from Process Dev to Mask write



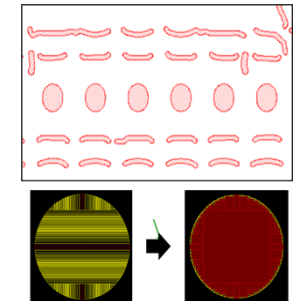
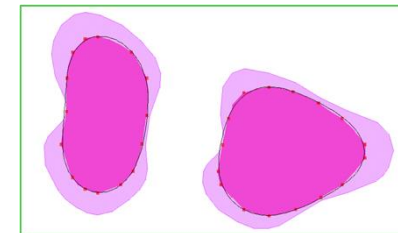
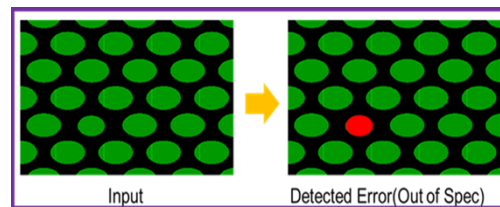
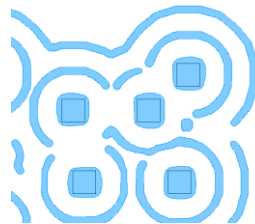
Full MEC Curvilinear Flow Support



Design

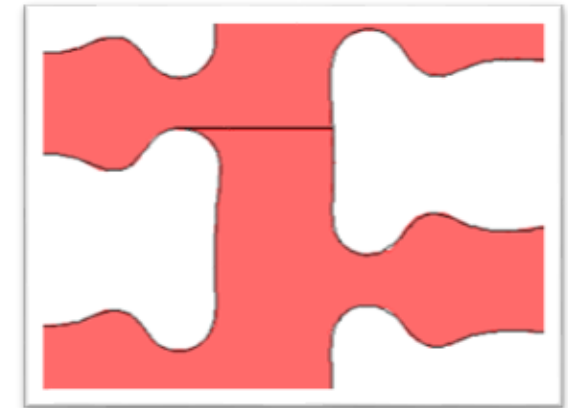
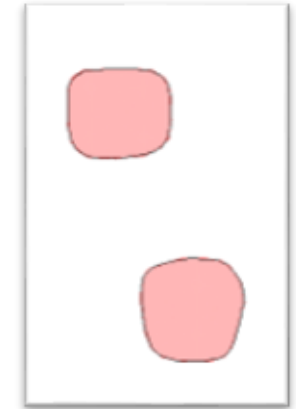
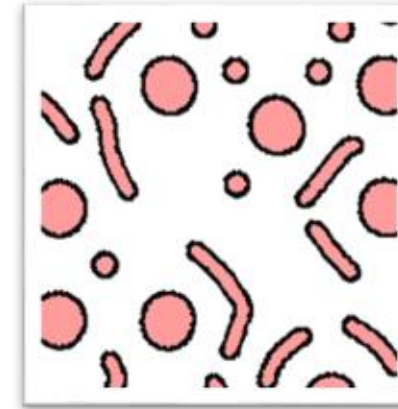


SEM-Contour



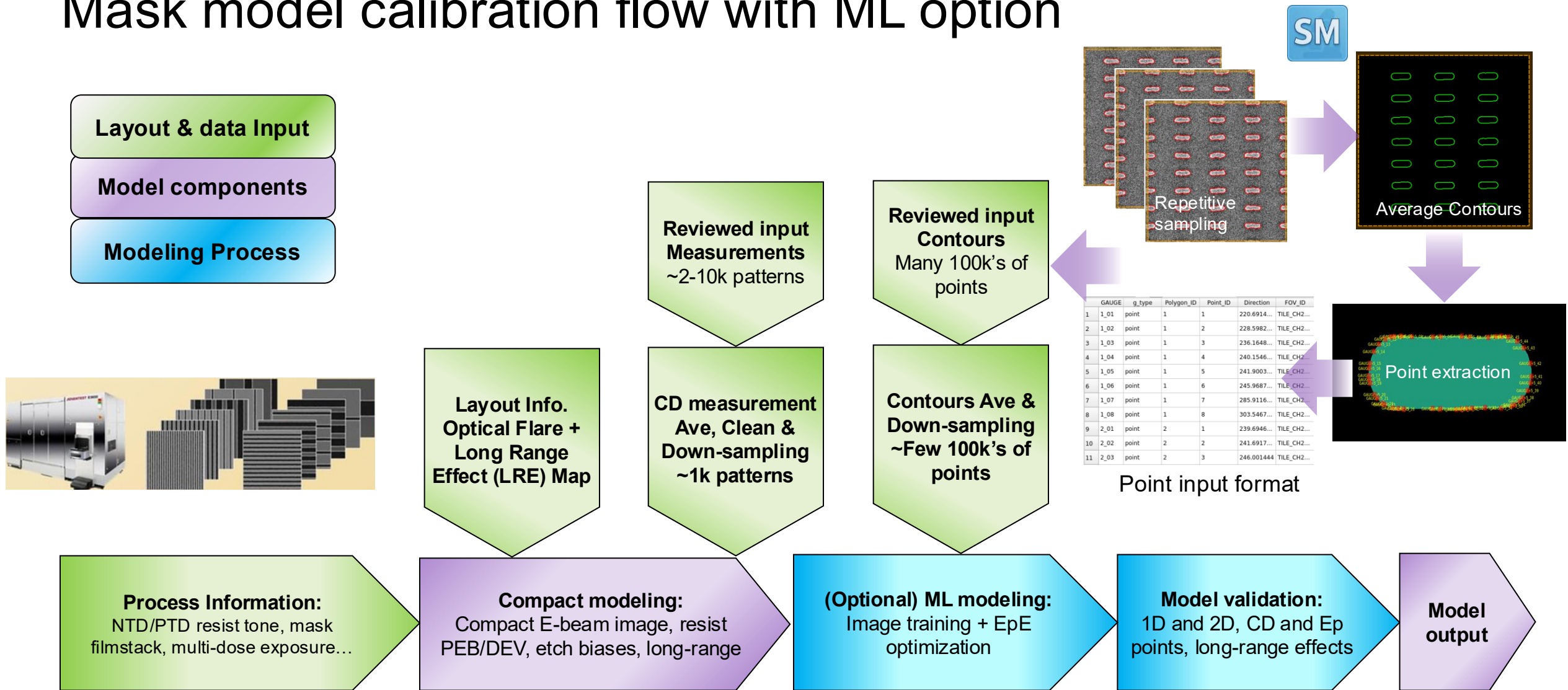
Experimental setup used in this work

- Manhattan and curvy test patterns used for MEC model calibration and validation
 - CD linearity, through-pitch, small to large areas, main and assist features, 2D complex shapes
- 3 different curvilinear layout types (w/ OPC not optimized for MEC)
 - Circular, semi-circular and rectangular main feature (MF) vias
 - Dense main features with curvy inside/outside corners
 - Main curvy features with small space-end to space-end
 - Small CD/area (high-curvature) circular and quasi-circular AFs
 - Long AFs w/bends and line-ends
 - Small jogs & bevels. Sharp and round corners with curvature $>$ mask process resolution
- Varied 2D pattern arrays E-beam written on mask for validating full flow MEC accuracy



Curve mask proximity data and modeling

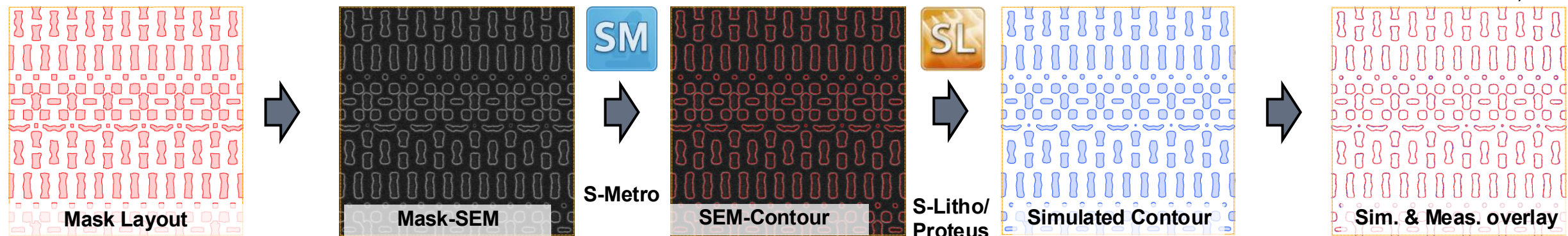
Mask model calibration flow with ML option



C-I. Wei et al. Machine Learning Enhanced Optical Proximity Correction Modeling for High-NA EUV Lithography. Proc. SPIE Vol. 13980, 2026
J. Bekaert et al. High-NA EUV mask CD-SEM metrology matching, and contour-based comparison of simulation result and wafer print. Proc. SPIE Vol. 13687, 2025

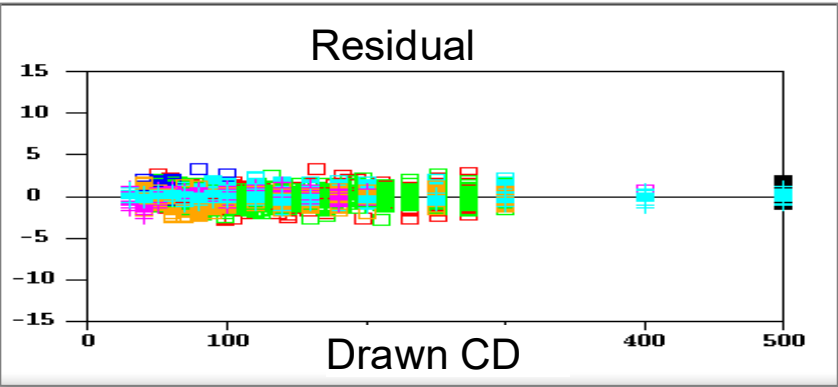
Curve mask patterns, data extraction & model calibration results

SEM extraction flow for model calibration & validation

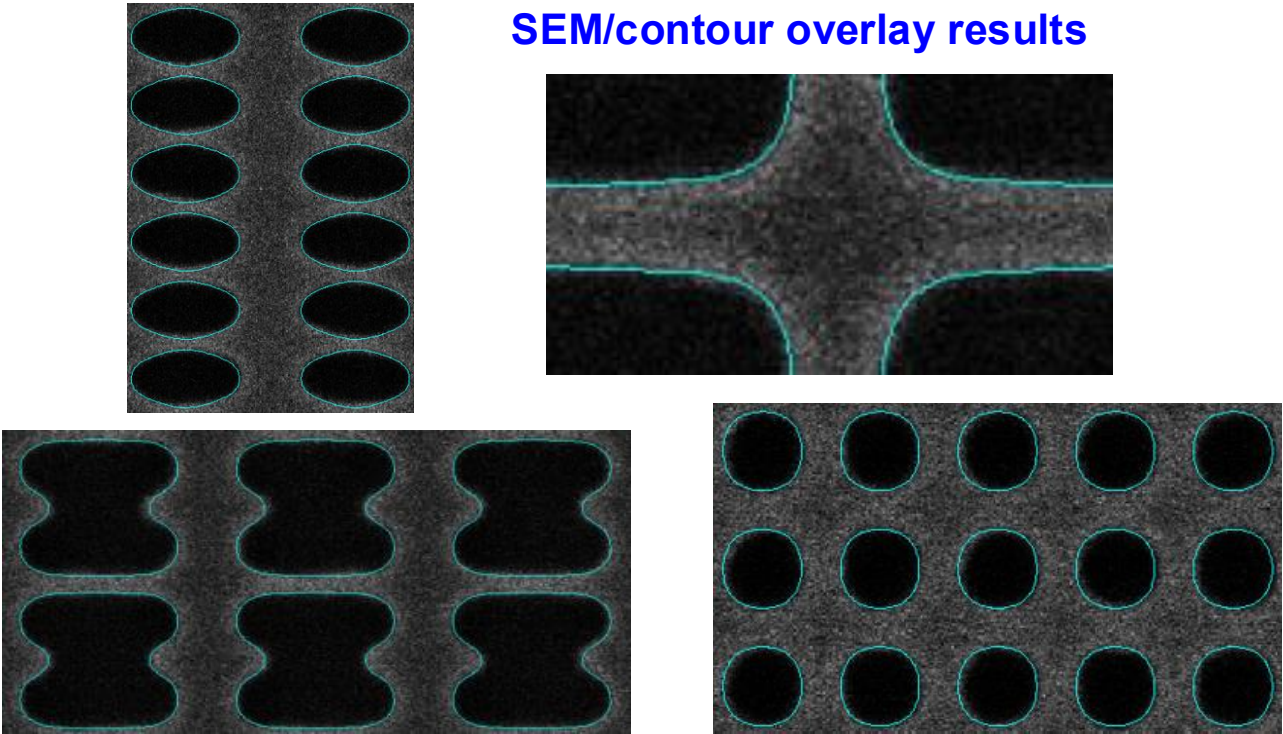


Model calibration results

Gauge Type	# Gauges	RMS
CD	4397	0.8nm
Edge point (EP)	147,777	1.0nm



SEM/contour overlay results

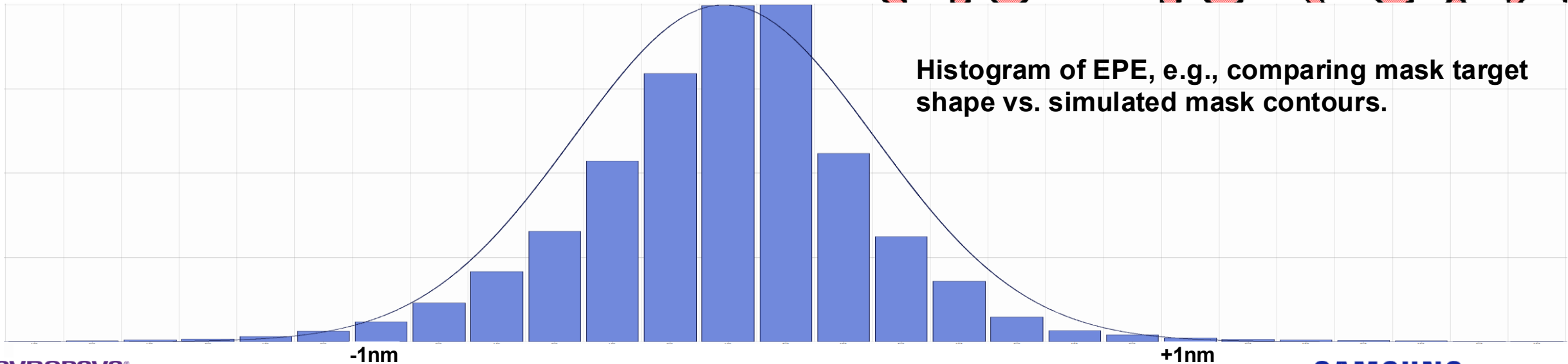
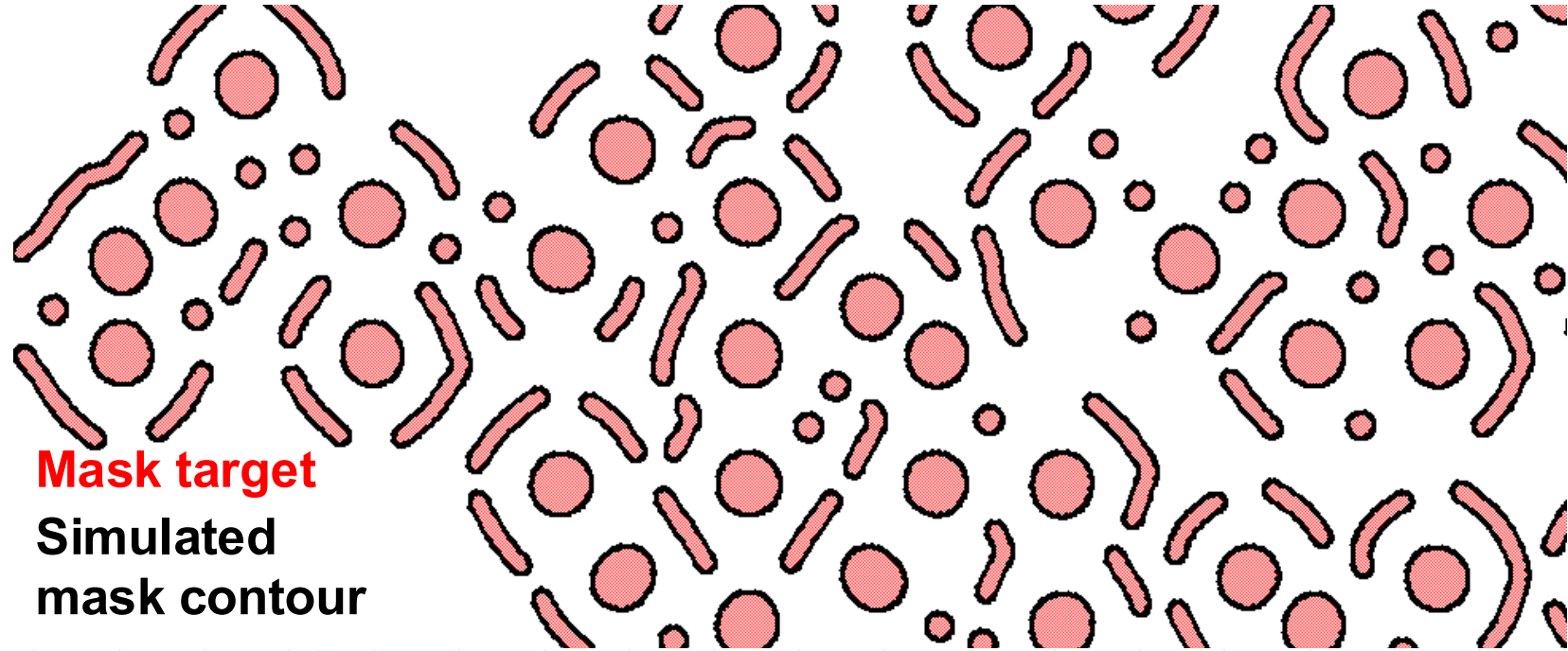


Target mask pattern vs. simulated MEC results

Curve layout1 – simulated contours vs target example

Simulated contours match the mask target well through many different pattern types:

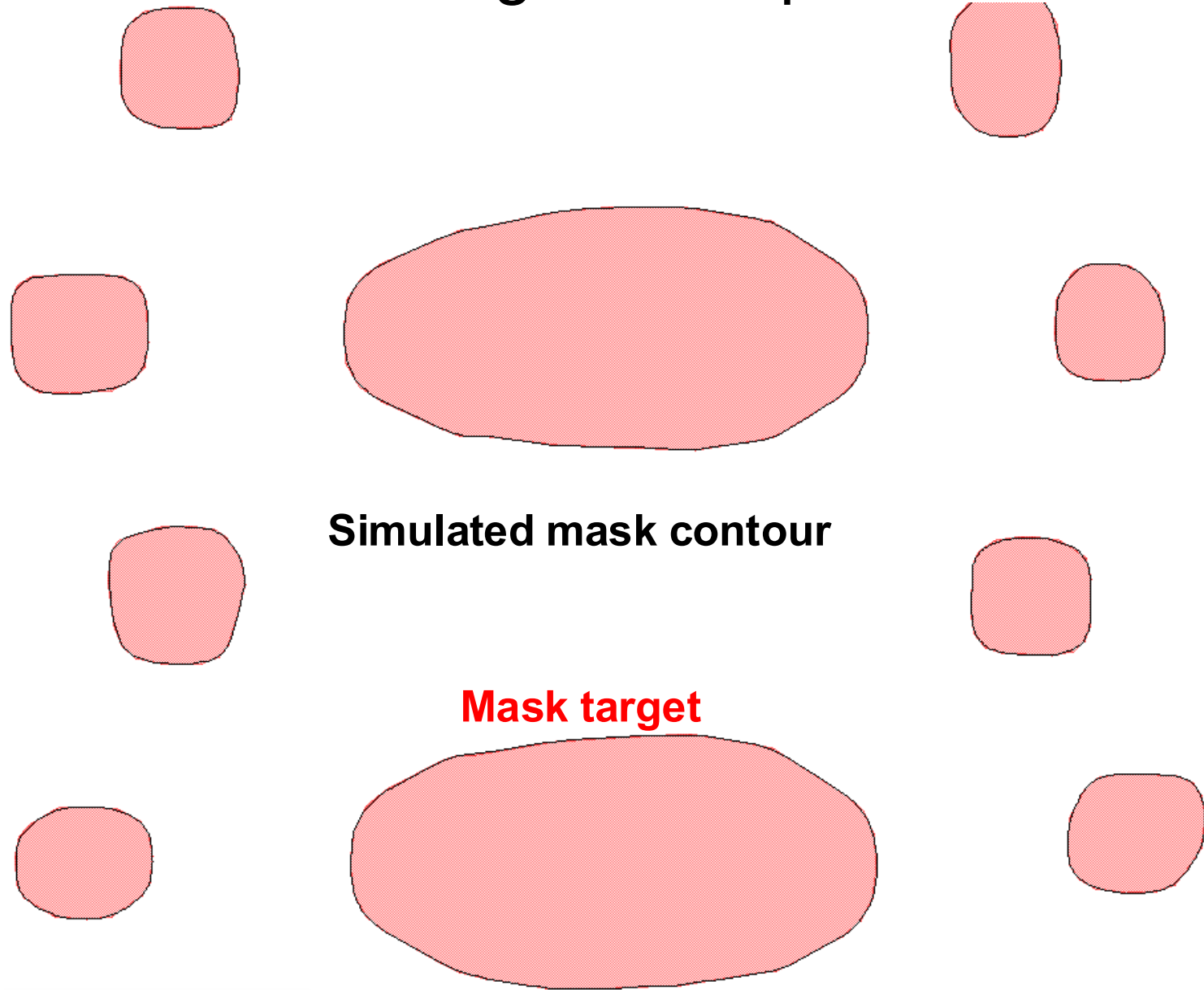
- large vias
- small circular Afs
- long AFs w/bends
- long AF line-ends



Curve layout2 – simulated contours vs target example

Simulated contours match the target mask well on additional pattern types:

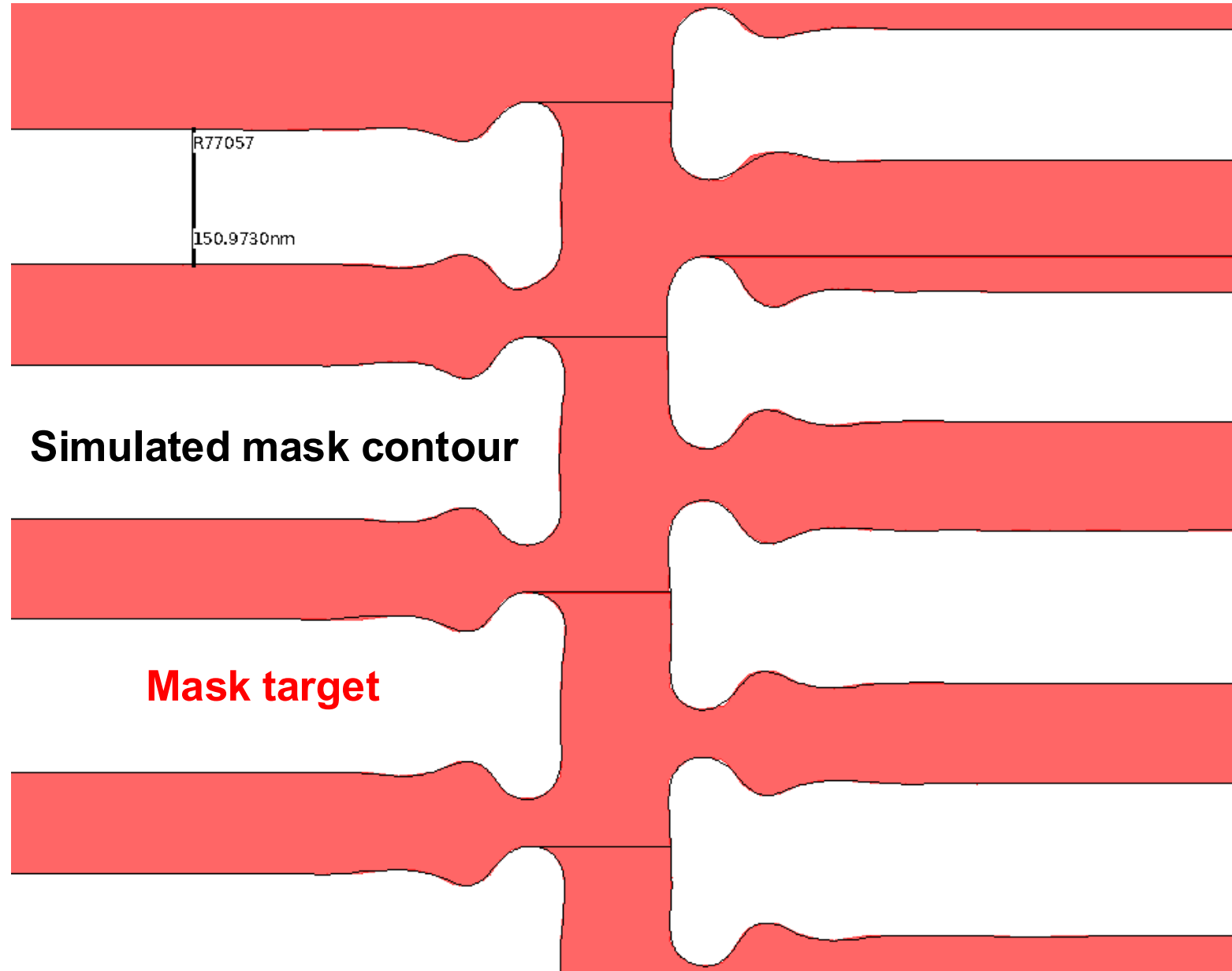
- large rectangular vias
- small quasi-circular AFs



Curve layout3 – simulated contours vs target example

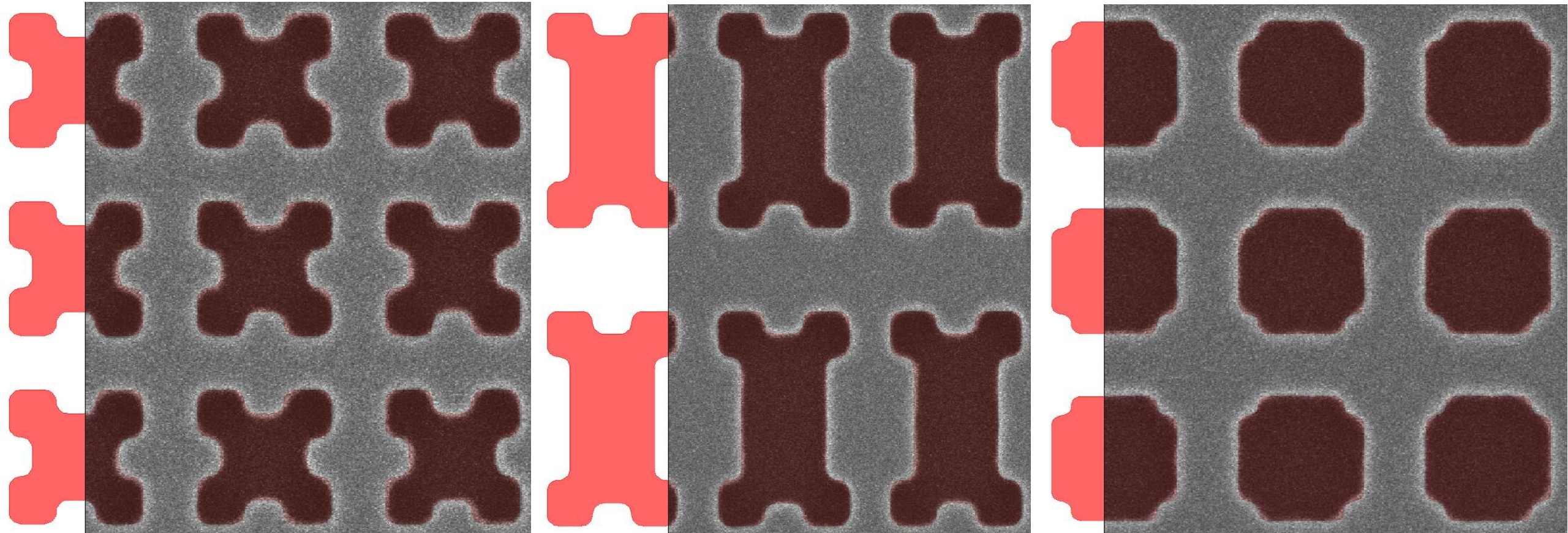
Simulated contours match the target mask well on additional pattern types:

- dense MF w/round corners
- MF space-end to space-end



Post-MEC mask SEM images show very good shape fidelity

- Mask written with MEC model & MEC correction described above

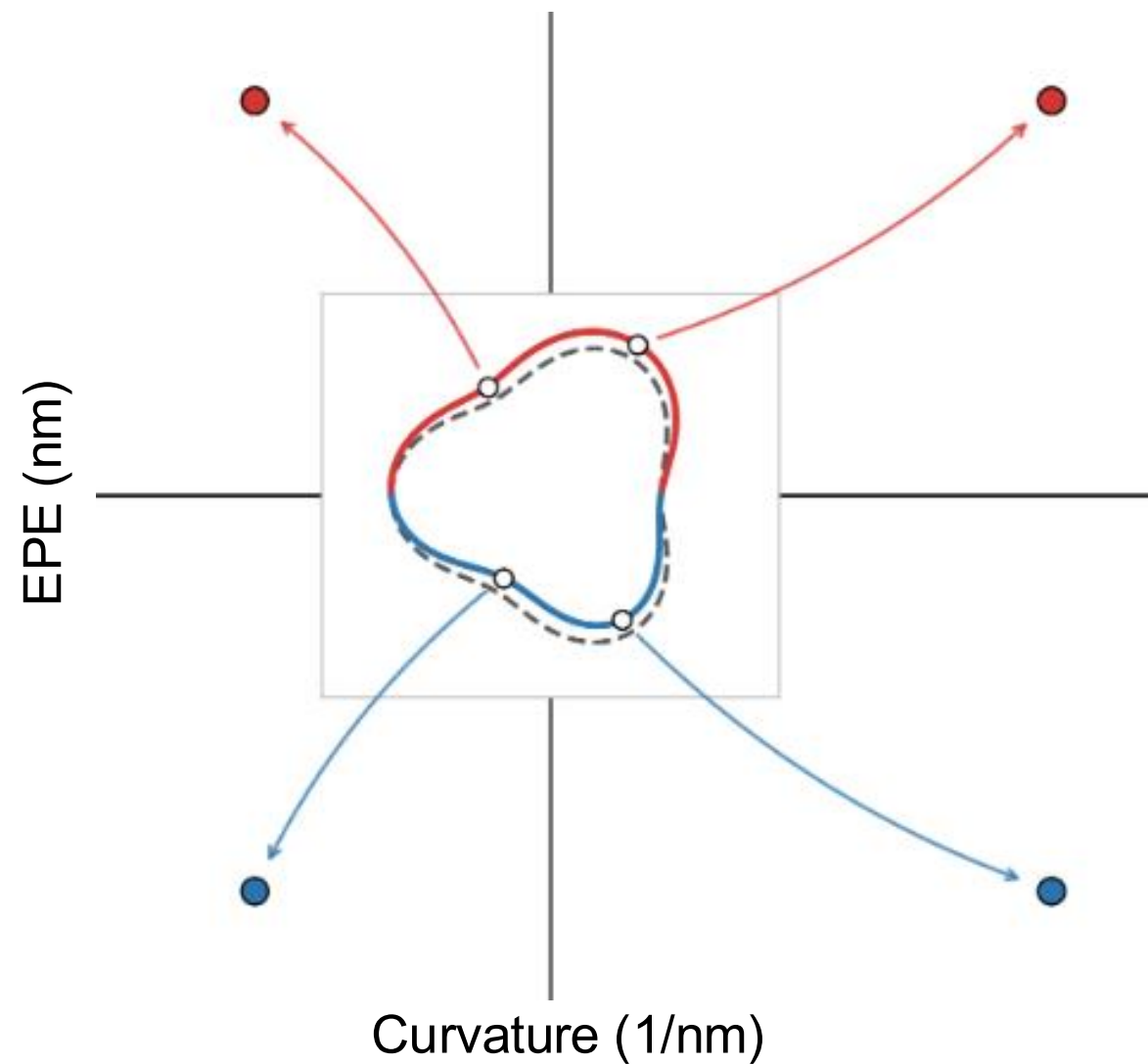


Patterns show improved curve and squared features resolution vs POR

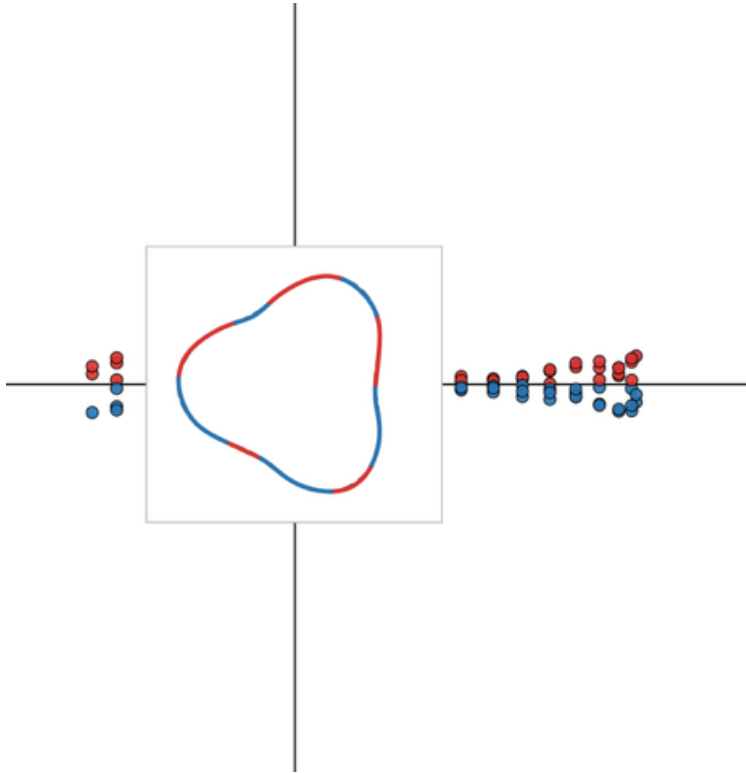
Min width and space in features varies from ~30-60nm (4X)

S-Metro for MEC results visualization & analysis

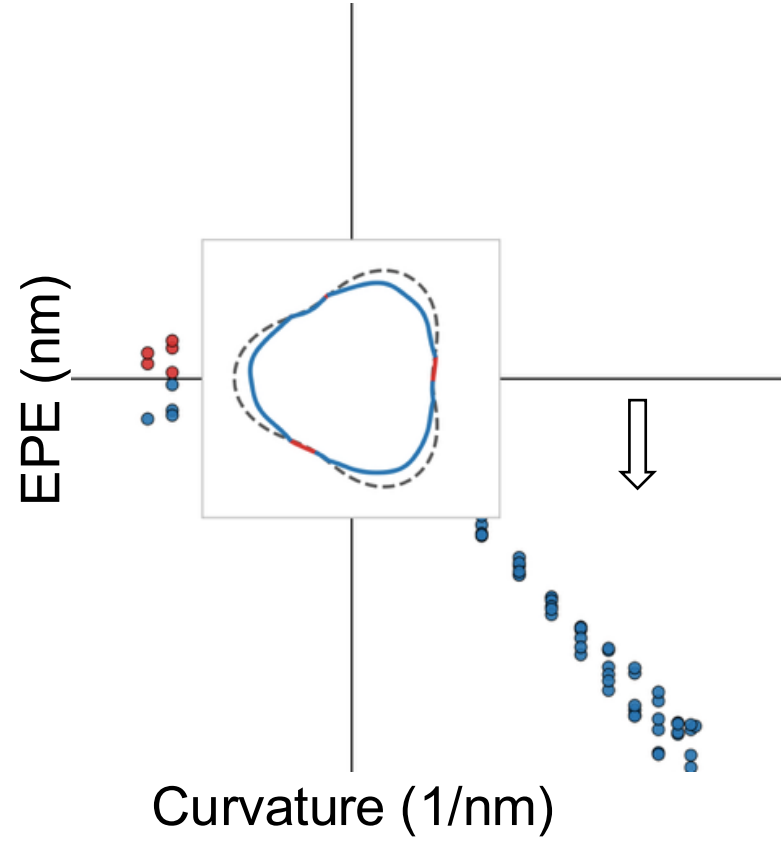
EPE vs Curvature visualization



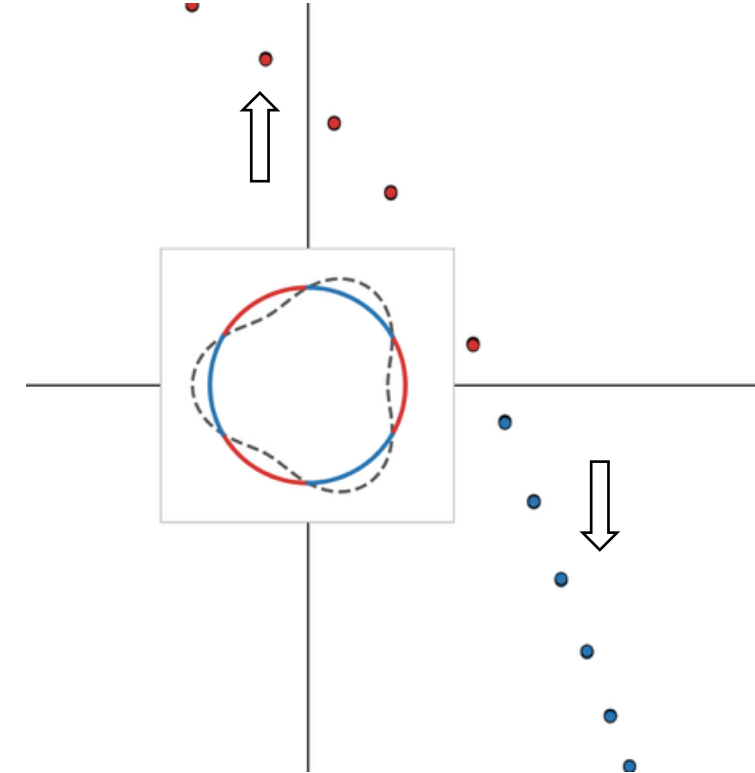
Matched to target



Convex pull-in



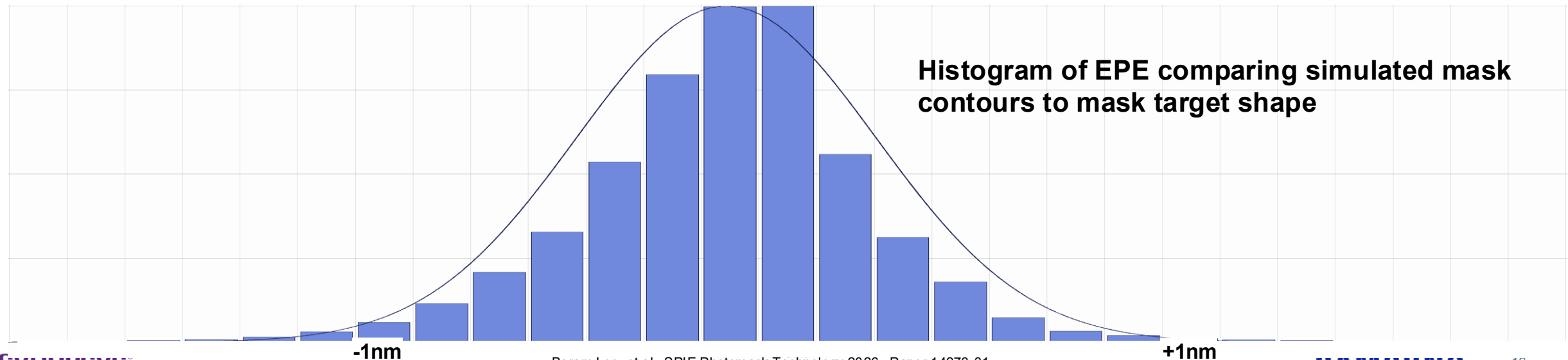
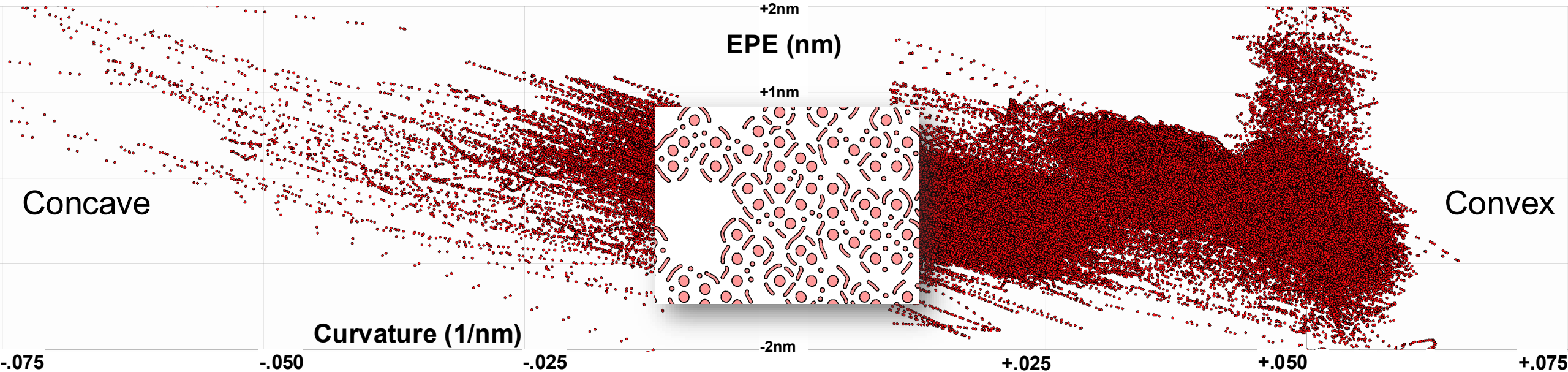
Extreme rounding



Analysis methods for comparing contours to mask targets



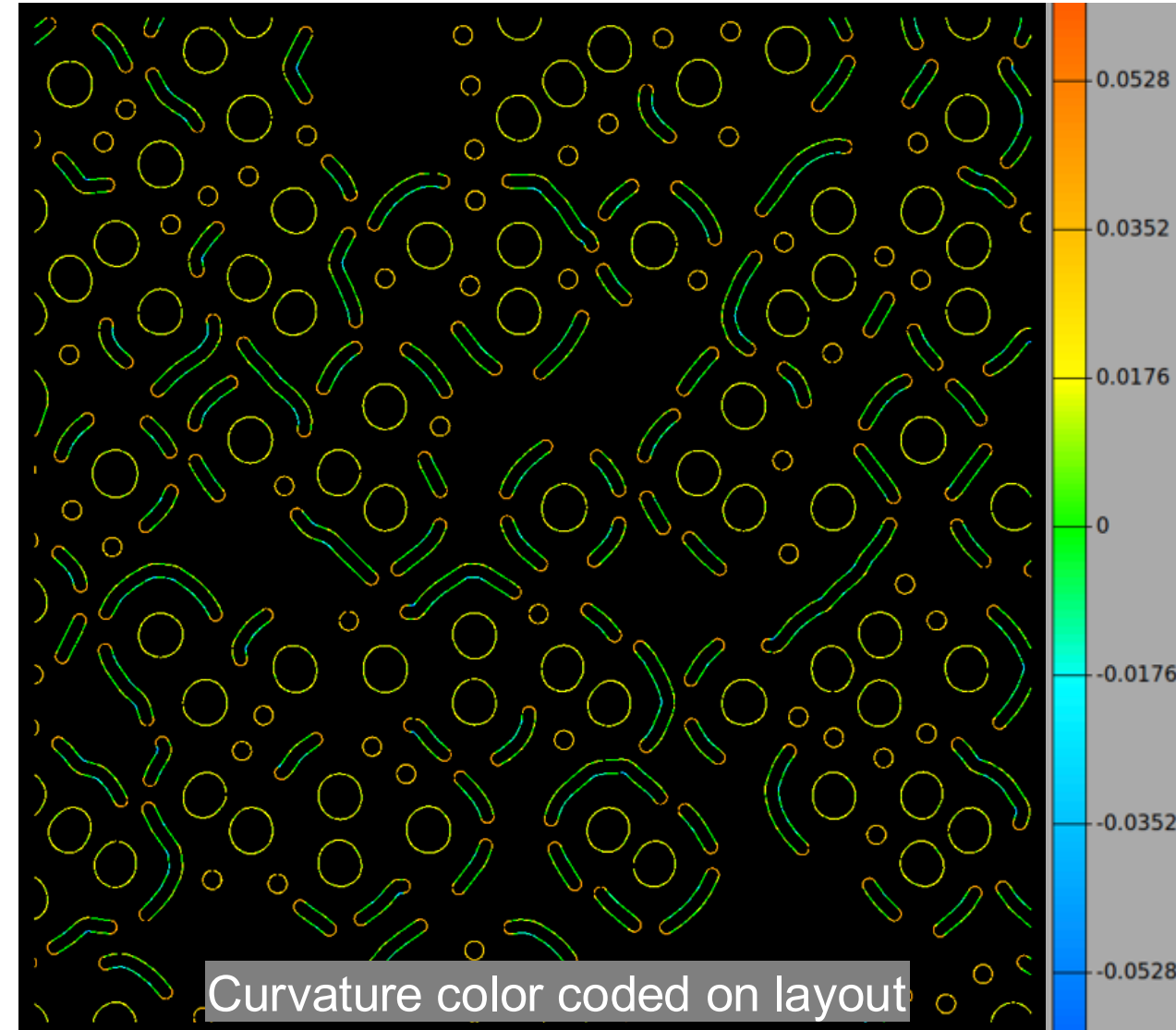
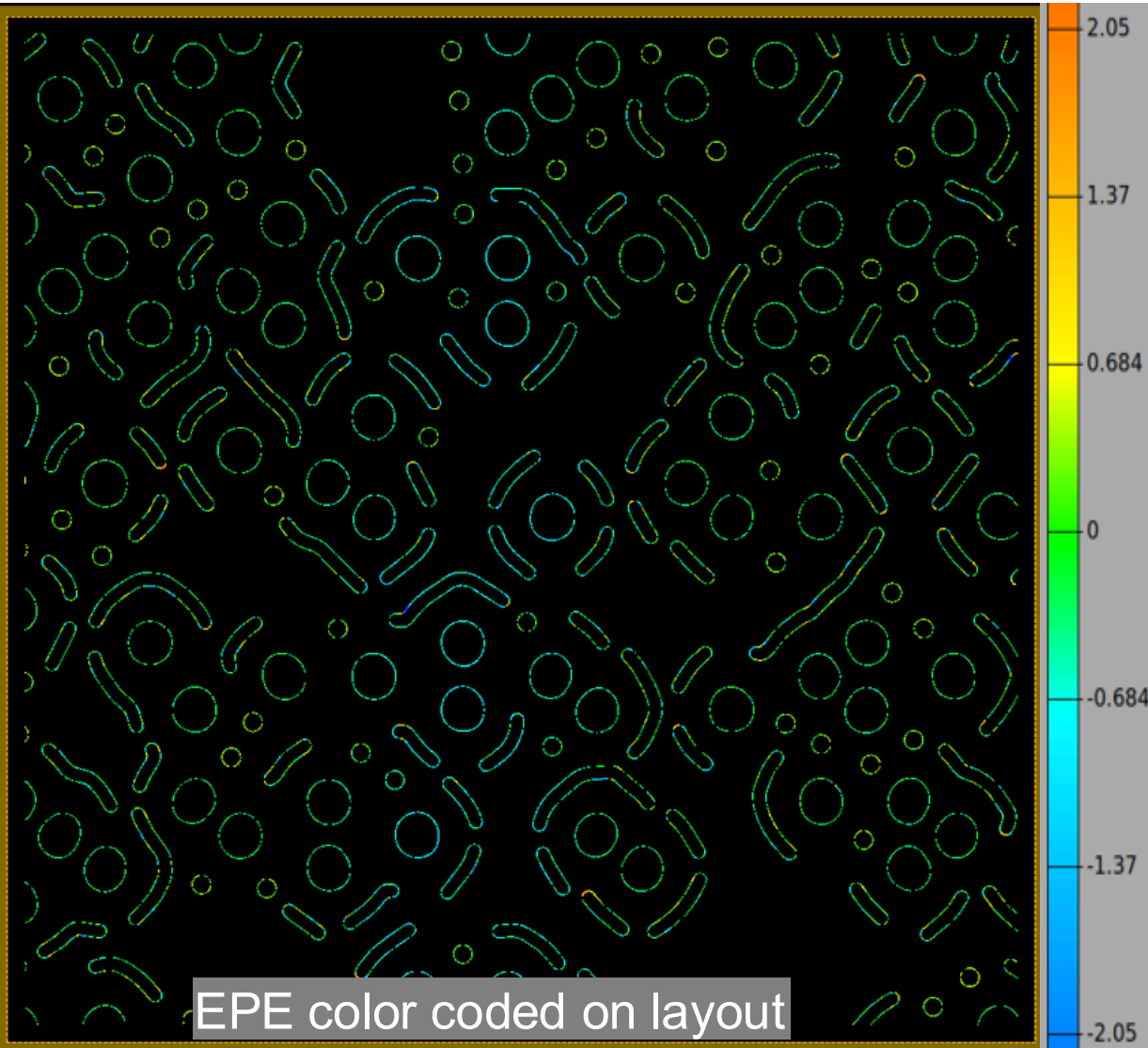
EPE vs Curvature plot comparing simulated mask contours to mask target shape



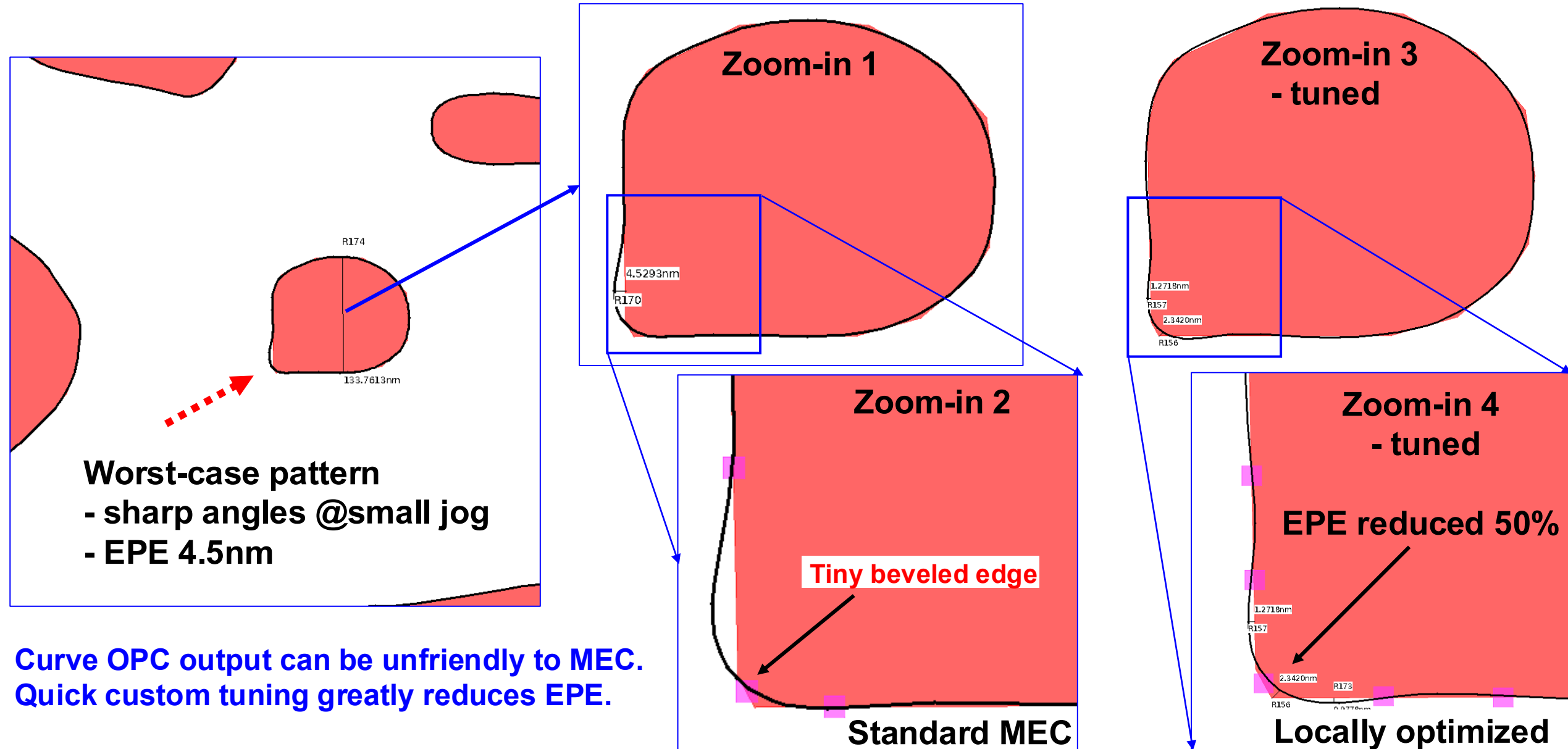
Further analysis methods for understanding MEC results



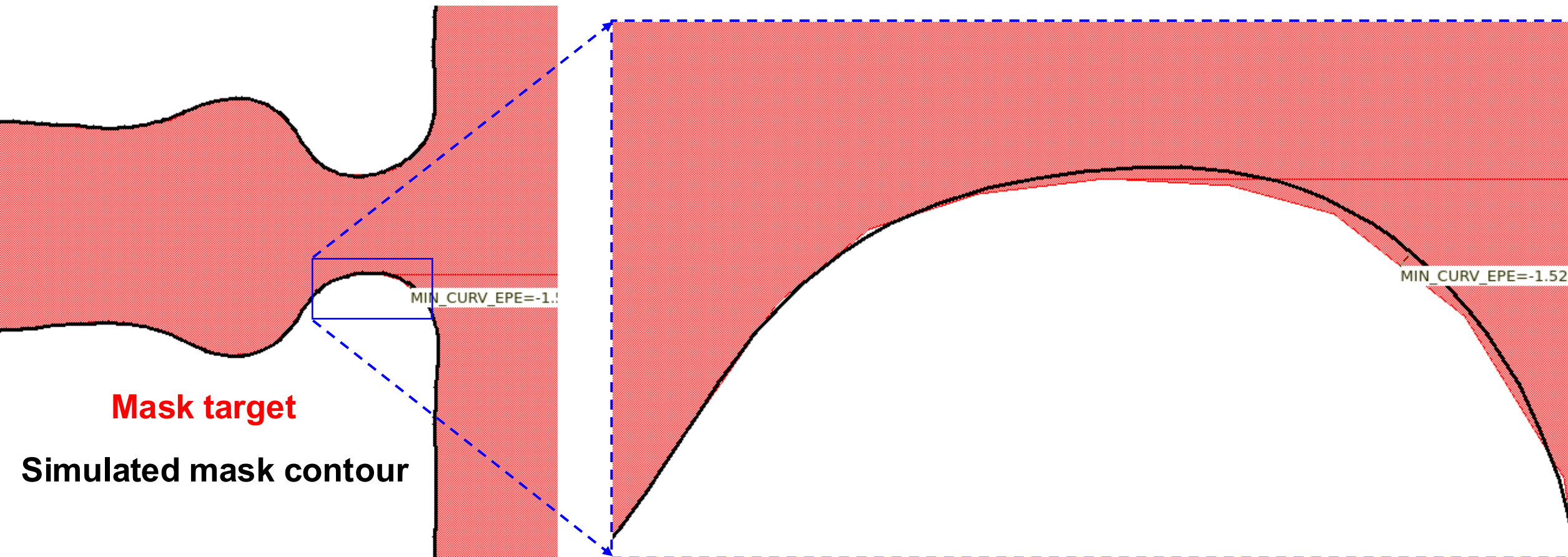
→ EPE & curvature contours to mask target



Tuning MEC for unfriendly post-OPC curvilinear shapes



Higher curvature may lead to weak convergence areas



Verification results for entire layout (PLRC): Goal: $\text{abs}(\text{EPE}) \leq 1.0\text{nm}$

For those errors w/ $\text{abs}(\text{EPE}) > 1.0$:

→ Ave EPE offset: $+0.03\text{nm}$

→ 95% (pos) $< 1.30\text{nm}$, → 95% (neg) $> -1.15\text{nm}$

Curve MEC output file size

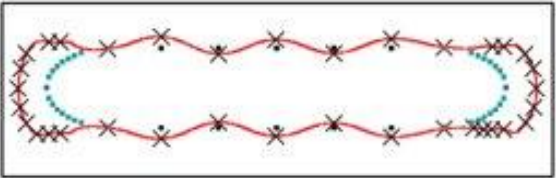
Optimization of MEC output file size

Balanced vertex density can enable 50% decrease on large designs

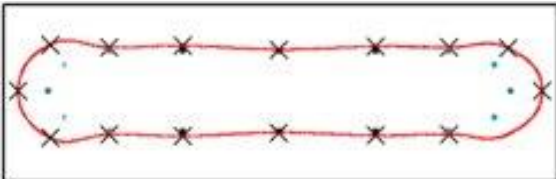
increase/decrease

Flow Stage	Layout	File Size
MEC Input - post-OPC	#1	1,958K bytes
	#2	4,397M bytes
	#3	824K bytes
MEC Output - medium vertex density	#1	4,111K bytes
	#2	3,982M bytes
	#3	1,147K bytes
MEC Output - medium vertex density	#1	1,647K bytes
	#2	1,788M bytes
	#3	562K bytes

MEC vertex densities



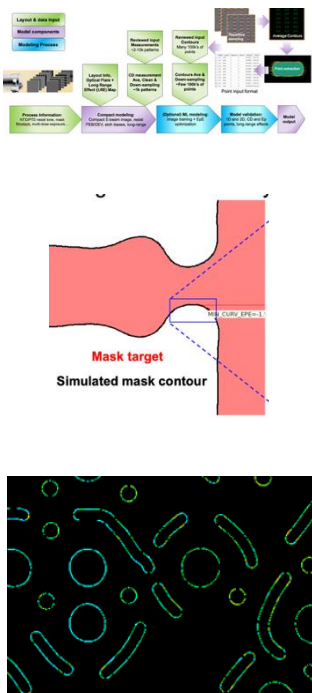
Medium Vertex density



Lower Vertex density

Summary

- Industry needs full-flow curvilinear solutions from Process Dev. to Mask write
 - Calibration data collect/analyze, mask/wafer modeling, MEC correction, MRC, verify, fracture
- Demonstrated an ML-ready MEC model data/calibration flow & accurate curvilinear MEC model results
- Applied curvilinear MEC solution to multiple layouts & validated with fabrication
- Used S-Metro to analyze the accuracy of MEC solution on a wide variety of pattern types in challenging curvilinear post-OPC layouts
- Applied intelligent vertex density to control file size with the curvilinear MEC solution



Thank You