SYNOPSYS®
Predictable Success

Lynx Design System Accelerates picoChip's Migration to 40nm

picoChip
flexible wireless

In the competitive consumer landscape, with each new generation of products, customers expect higher levels of performance and functionality at a lower cost. For picoChip to drive the pace of these markets, rapid adoption of new technologies is essential. Synopsys' Lynx Design System helped accelerate our migration to 40-nanometer, saving at least 8 weeks of R&D schedule on our next generation design."

Will Robbins,
VP Silicon picoChip

Established in 2000, picoChip provides signal processing products for wireless communications – in particular femtocells. A leader in multi-core DSP, picoChip has established an award-winning portfolio of cost – and performance – optimized silicon devices. Because picoChip's product strategy is to deliver dramatically better performance-per-dollar than legacy processors or FPGAs, they rely on the adoption of new technologies in a quick, cost-effective manner.

Based on prior experience, picoChip was concerned about the cost of creating and maintaining a new production flow for their first 40nm design. They had made significant investments integrating new tools and methodologies in previous process node migrations and needed to streamline this effort to meet an aggressive schedule goal for their newest design.

Synopsys Solution

- Lynx Design System
- Synopsys Professional Services

Cooperation Benefits

By adopting Synopsys' Lynx Design System, picoChip was able to bring up a working 40nm flow in about 2 weeks – one fifth the time of previous new flow deployments and at a fraction of the cost. Since the latest tools and design methods are embedded in Lynx, the joint design team of picoChip and Synopsys engineers were able to immediately focus on optimizing their new design to meet performance targets.

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