Corporate Overview for Investors

February 2020
Forward-Looking Statements

This presentation contains forward-looking statements under the safe harbor provisions of Section 21E of the Securities Exchange Act of 1934, including, but not limited to, financial targets for the second quarter and full fiscal year 2020, projected financial results; long-term growth rates; expected share count; revenue variability; hardware growth; industry growth rates; software trends; long-term objectives; business outlook, opportunities, and strategies; customer and market expansion; planned acquisitions and buybacks; planned product releases and capabilities; and the expected impact of the U.S. government action placing certain entities on the “Entity List” on our financial results. These statements involve known and unknown risks, uncertainties and other factors that could cause actual results to differ materially from those expressed or implied. Accordingly, we caution stockholders and prospective investors not to place undue reliance on these statements. Such risks include, but are not limited to: the anticipated timing and customer adoption of our new product offerings; additional administrative, legislative or regulatory action by the U.S. or foreign governments, such as the imposition of additional tariffs or export restrictions, which could further interfere with our ability to provide products and services in certain countries; the response by current or potential customers and their willingness to purchase products and services from us in the future; uncertainty in the growth of the semiconductor and electronics industries; consolidation among our customers and our dependence on a relatively small number of large customers; risks and compliance obligations relating to the global nature of our operations; continued uncertainty in the global economy; fluctuation of our operating results; increased variability in our revenue due to ASC 606 implementation, including the resulting increase in recognizing upfront revenue as a percentage of total revenue; our highly competitive industries and our ability to meet our customers' demand for innovative technology at lower costs; cybersecurity threats or other security breaches; our ability to protect our proprietary technology; our ability to realize the potential financial or strategic benefits of acquisitions we complete; and others. More information on potential risks, uncertainties and other factors that could affect Synopsys’ results is included in filings it makes with the Securities and Exchange Commission from time to time, including in the sections entitled “Risk Factors” in its Annual Report on Form 10-K for the fiscal year ended October 31, 2019 and in its latest Quarterly Report on Form 10-Q. The information provided herein is as of February 19, 2020. Synopsys undertakes no duty, and does not intend, to update any forward-looking statement, whether as a result of new information, future events or otherwise, unless required by law.

Regulation G

In addition to financial results prepared in accordance with Generally Accepted Accounting Principles, or GAAP, this presentation will also contain certain non-GAAP financial measures. Except for certain forward-looking non-GAAP financial measures for which a reconciliation is not possible without unreasonable efforts, reconciliations of the non-GAAP financial measures contained in this presentation or given verbally to their most comparable GAAP measures are included in the table at the end of this presentation, along with the first quarter of fiscal year 2020 earnings release and financial supplement, dated February 19, 2020, and available on Synopsys' website at https://www.synopsys.com/company/investor-relations/financial-news.html.
Synopsys: The Market & Technology Leader

<table>
<thead>
<tr>
<th>Broad Product Portfolio</th>
<th>FY19&lt;sup&gt;(1)&lt;/sup&gt;</th>
<th>FY20 Targets&lt;sup&gt;(2)&lt;/sup&gt;</th>
</tr>
</thead>
<tbody>
<tr>
<td>Electronic Design</td>
<td>$3.361B</td>
<td>$3.60B - $3.65B</td>
</tr>
<tr>
<td>Automation (EDA)</td>
<td>8%</td>
<td>7% - 9%</td>
</tr>
<tr>
<td>Semiconductor</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Intellectual Property (IP)</td>
<td>25%</td>
<td>~27%</td>
</tr>
<tr>
<td>Software Security &amp; Quality</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>$4.56</td>
<td>$5.18 - $5.25</td>
</tr>
<tr>
<td></td>
<td>$801M</td>
<td>$800-825M</td>
</tr>
<tr>
<td>Business Model:</td>
<td></td>
<td>Time-based, recurring revenue model</td>
</tr>
</tbody>
</table>

(1) Revenue grew 9% y/y adjusting for the additional week in fiscal FY18
(2) These targets were provided by Synopsys as of February 19, 2020, and are not being updated at this time; a GAAP to non-GAAP reconciliation of FY20 Operating Margin targets and FY20 EPS targets are included at the end of this presentation
Synopsys: The Market & Technology Leader

- **Software Integrity**
  - 5 years
  - ~10% of revenue
- **Semiconductor IP**
  - >15 years
  - 20%+ of revenue
- **Electronic Design Automation**
  - >30 years of investment and execution
  - ~65% of revenue

**Emerging Leader, Broadest Portfolio**
Growth: ~15-20%*

**#2 IP Vendor**
Growth: low double-digits*

**Market Leader**
Growth: mid-high single-digits*

* These multi-year objectives are provided as of February 19, 2020
Long-Term Financial Objectives*

• While the results in any given period may vary due to acquisitions or other near-term priorities, our primary long-term objective is to drive double-digit non-GAAP EPS growth on a multi-year basis, through a mix of the following elements:

• Grow total revenue generally in the high single-digit range
  – EDA revenue generally in the mid-to-high single-digits
  – IP & System Integration revenue generally in the low double-digits
  – Software Integrity revenue exceeding market growth, generally in the 15-20% range as market evolves

• Drive non-GAAP operating margin into the high-twenties by 2021 and 30% range longer-term

• Utilize our strong cash flow primarily for M&A and share buybacks

* These multi-year objectives are provided as of February 19, 2020
Synopsys: The Market & Technology Leader

Uniquely Positioned for Stability and Growth

• Electronics complexity driving growth
• Clear technology leadership
• Financial strength and growth
Global Value Chain – 2019E
EDA & IP – at the Heart of Accelerating Electronics Innovation

Sources: IC Insights, VDC Research, Synopsys Internal Estimates
Blue Chip, Diversified Customer Base

**Semiconductors**
- Consists of virtually all semiconductor companies in the world, including all of the top 20
- One >10% customer in FY19
- Investment in developing highly complex chips is a clear priority for customers, whether at advanced or more established process nodes

**Systems**
- ~40% of our revenue comes from systems companies
- Systems companies develop chips as an input to an end product, rather than selling the chip itself
- Verification challenges are rapidly growing at the intersection of hardware and software

**Software Developers**
- Key function across all industries: semis and systems, to enterprises in segments such as financial services, medical devices, automotive, and industrial
- Targeting the 20M+ SW developers
- Immense software complexity challenges plus high impact of security vulnerabilities
New Technology is Impacting Every Vertical
Artificial Intelligence Market Growth

Chipsets across all markets will include Deep Learning Capabilities

- **Mobile** - All premier smartphones will integrate AI Processing Capabilities by 2021
- **Data Center** - More than 50% of enterprises will deploy AI accelerators in their server infrastructure by 2022
- **Auto** - Volume production of autonomous vehicles will begin in 2020
- **IoT** - More than 20% of IoT devices will have AI Processing Capabilities by 2022
Enabling and Leveraging the Potential of AI
Synopsys tools incorporate AI/ML technologies while enabling AI processor development

Design and verification tools for AI processors
- Multiple years of collaboration alongside AI pioneers – deep understanding of AI design implications and challenges
- Verification solutions from architecture to simulation to emulation and prototyping
- Digital Design Platform with Fusion Technology™ significantly enhanced to address AI processor design challenges

Leading AI-based chip design technologies
- Design tools with state-of-the-art machine learning technology to address extreme complexities of leading-edge designs
- “Synopsys’s PrimeTime with new machine-learning technology allows us to complete ECO runs in hours versus the days it took before, and reuse learnings across blocks, hierarchies, and even design styles.” - Renesas

IP solutions designed for AI applications
- Proven IP solutions accelerate time-to-market
- Memory/interface/analog IP designed to handle the uniquely intensive AI demands of memory capacity, bandwidth and coherency
- Embedded vision processors with safety enhancement packages
Automotive

OWNERSHIP & INFRASTRUCTURE
Entire Ecosystem

AUTONOMOUS DRIVING
Systems of systems

ARTIFICIAL INTELLIGENCE
Big Data, Capture, Analyze, Decision Making

SOFTWARE
>150M Lines of Code per vehicle

SYSTEMS
Infotainment, GPS, Drivetrain, Sensors

CHIPS
4 Billion transistors

Secure
New Electronics Architecture Required
Integrated Compute and Smart Sensor Architecture Requires Redesign

- Integrated Controllers per Domain
- Sensor Fusion
- Connectivity and upgradeability
- Highest reliability, security and safety

Domains

- Powertrain
- ADAS
- Comfort
- Infotainment / V2X
- Gateway

Functional Safety
Security
Reliability (Silicon Aging)
SW Development (Early SW, Secure, OTA validation)
AI/Deep Learning
Low Power

Requirements
Automotive Opportunity: Software Security & Quality

11 of the top 15
Automotive OEMs use Synopsys Software Integrity

<table>
<thead>
<tr>
<th>Static Analysis</th>
<th>Security Testing</th>
<th>Supply Chain Management</th>
<th>Security Services</th>
</tr>
</thead>
<tbody>
<tr>
<td>Find critical defects and vulnerabilities in code</td>
<td>Find vulnerabilities before hackers</td>
<td>Find known vulnerabilities in open-source &amp; 3rd party code</td>
<td>Gap analysis / remediation planning</td>
</tr>
<tr>
<td>Automotive compliance (MISRA, ISO26262)</td>
<td>Fuzzing for automotive protocols</td>
<td>Supply chain total health with SW BoM</td>
<td>Remediation plan execution</td>
</tr>
<tr>
<td>OWASP Top 10 and CWE Top 25</td>
<td>Ethernet, WiFi, CAN, Bluetooth, SMS, DHCP</td>
<td>Secure integration of connected systems</td>
<td>Ongoing program execution</td>
</tr>
</tbody>
</table>
Advanced Process Development Continues

AMD Zen+ 7nm "Gray Hawk" APU To Be Out In 2019 | 4 Cores, 8 Threads & 10W, Reports say

All 2020 iPhones To Reportedly House 5nm Chips With X55 5G Modem

Tesla’s In-House SoC Is Built On 14nm, Has A 260mm² Area & 2100FPS

Samsung’s Aggressive EUV Plans: 6nm Production in H2, 5nm & 4nm On Track

Intel Will Launch 7nm Chips in 2021, Ice Lake Ships in June

Samsung Unveils The World’s First 10-nm FinFET Semiconductor Technology

TSMC starts constructing facilities to turn out 3nm chips by 2023

ARM lays out plans for 5nm “Hercules” core design by 2020
Synopsys: The Market & Technology Leader

Uniquely Positioned for Stability and Growth

- Electronics complexity driving growth
- Clear technology leadership
- Financial strength and growth
Synopsys: Silicon to Software

**Software**
- Application security testing & quality
- Leader in Gartner’s Magic Quadrant

**Verification**
- Fastest engines & unified platform
- HW/SW verification & early SW bring-up

**IP**
- Broadest portfolio of silicon-proven IP
- #1 interface, embedded mem. & physical IP

**Design**
- Digital & custom AMS platforms
- Best quality of results & highest productivity

**Silicon**
- TCAD, lithography tools & yield optimization
- Down to 5nm & below
• Market and technology leader in EDA, the tools and technologies used to design semiconductor chips
  ~$7.3 billion market*; generally tracks semiconductor R&D spend over time

• All leading semiconductor companies in the world use Synopsys technology

• State-of-the-art design and verification platforms with unmatched global support

* Source: IBS 2019 forecast
Synopsys Fusion Design Platform™

Trusted Anchor Products, Correlated Platform with Synergistic Value Links

- #1 Synthesis, #1 P&R, #1 Signoff
- Machine Learning Technologies
- Massively Parallel Architecture

- Deep Integrations Across Platform
- In-Design Physical Signoff
- Co-Design Custom Implementation
Synopsys Fusion Compiler™

- Only single product on market with innovative next-generation integration of synthesis, place & route and key signoff technologies
- Built with market-leading products
- Redefines conventional product boundaries; sharing of algorithms, code and data model
- New synthesis engine, developed from ground up for significantly higher capacity & performance
- Early stages of multi-year product ramp. Driving considerable benchmark wins and competitive displacements with broad set of customers
Synopsys Custom Compiler™ Adoption Growing
#1 in Digital/Functional Verification

Verification Continuum™ Platform

- Complete platform with #1 products in all categories
- Industry’s highest-performance engines
- Uniquely positioned for early software bring-up
- Domain-specific solutions for automotive, 5G and AI designs
- Accelerating innovation with investments in cloud and machine learning technologies
Verification Hardware: ZeBu® Server 4 Emulation System

Leading system for software bring-up
Only modern system to allow software-on-hardware validation

Industry’s fastest emulation system
Now 2x higher performance

Industry’s largest capacity
Scalable to 19B+ gates

Industry’s lowest total cost of ownership
5x lower power consumption, half data center footprint
#1 in Analog/Mixed Signal Simulation

- Market leader for >20 years
- Close collaboration with leaders in memory, automotive IC and high-performance computing
• #2 market share in IP, the reusable building blocks that are used for chip designs
  – Leading supplier of interface, memory, analog and physical IP
  – ~$4.1 billion market*, benefitting from our customers’ move to outsourcing more and more complex IP

• IP reuse reduces our customers’ chip design costs, lowers risk and speeds time-to-market

• We’re investing in new markets (IoT, Automotive, Security, Cloud Computing) & latest processes and protocols

• >15 years of investment and commitment resulting in unmatched portfolio breadth, quality and reliability

* Source: IBS 2019 forecast
Synopsys DesignWare® IP

- Broadest portfolio available in the market; FY19 revenue >$750M
- Covers vast majority of chip
- Growing demand for more integrated subsystems
- New markets drive new opportunities
- Customers count on us for the long haul to accelerating time-to-market and reduce risk

* Small boxes are standard cell library elements.

Synopsys Portfolio
New Markets: More Functionality in Key Markets
Driving New Technology Requirements

- **AI**
  - Machine Learning, Deep Neural Networks

- **AUTOMOTIVE**
  - ADAS, Infotainment, Connected Vehicle

- **CLOUD**
  - Networking, Servers AI Accelerators

- **IoT**
  - Smart Home, Industrial, Wearables
• Entered the software security & quality space in 2014
  – Since then, have assembled and developed the broadest portfolio of solutions on the market; $335 million revenue in FY19
  – Solutions allow software code developers to fix security and quality defects early in development
  – Approximately $2.5 billion+ served market: fragmented, with numerous point solutions; recognition of cost of software failure has increased dramatically

• Synopsys Polaris Software Integrity Platform™
  – The most advanced and complete set of security and quality tools for every step of the software development lifecycle
Software Security & Quality Market

- Evolving, nascent market
- Growth drivers:
  - Omnipresence of software
  - Widespread connectivity exacerbates security issues
  - Significant business risk from breaches
  - Large development teams
- Market growth rate: mid-teens %
- Synopsys multi-year growth rate in excess of market rate: ~15-20% range*

TAM: ~$4.5-6.5B
SAM: ~$2-2.5B

Sources: Gartner, Forrester, IDC, other external and internal analyses
* This multi-year objective is provided as of February 19, 2020
Growing the Software Integrity Platform - Acquisitions

- **Coverity**
  - Acquired 2014
  - Tools Acquired:
    - Quality & Security Advisor
    - Covertly
    - TA DEV

- **Codenomicon**
  - Acquired 2015
  - Tools Acquired:
    - Defensics Fuzzer
    - AppCheck

- **Protocode**
  - Acquired 2015
  - Tools Acquired:
    - Software Composition Analysis

- **Digital**
  - Acquired 2016
  - Services Acquired:
    - Professional Services
    - Managed Services
    - BSIMM
  - Tools Acquired:
    - SecureAssist
    - eLearning

- **Black Duck**
  - Acquired 2017
  - Tools Acquired:
    - Open source software management
    - Source code scanner
    - Comprehensive open source database

- **Kalistanck**
  - Acquired 2014
  - Tools Acquired:
    - TA QA

- **Seeker**
  - Acquired 2015
  - Tools Acquired:
    - IAST
    - R&D team from Quotium

- **Goanna**
  - Acquired 2015
  - Tools Acquired:
    - Goanna (Coverity)

- **Forcheck**
  - Acquired 2016
  - Tools Acquired:
    - Static analysis for Fortran

- **Tinfoil Security**
  - Acquired 2020
  - Tools Acquired:
    - Developer-friendly dynamic application security testing (DAST) for APIs and web apps
The Most Complete Portfolio in the Industry

<table>
<thead>
<tr>
<th>Static Analysis</th>
<th>Software Composition Analysis</th>
<th>Dynamic Analysis</th>
<th>Security Services</th>
</tr>
</thead>
<tbody>
<tr>
<td>Find critical defects and vulnerabilities in code</td>
<td>Find known vulnerabilities in open-source &amp; 3rd party code</td>
<td>Tests running apps for security vulnerabilities</td>
<td>Strategic analysis to build Software Security Initiative</td>
</tr>
<tr>
<td>Runs in the dev environment and DevSecOps workflow</td>
<td>Identifies license and compliance risks</td>
<td>Enables dev to fix security flaws early in the SDLC</td>
<td>DevSecOps integration injects security into dev workflow</td>
</tr>
<tr>
<td>OWASP Top 10 and CWE Top 25</td>
<td>Tests source and binaries in apps and containers</td>
<td>API Scanner for IoT devices and mobile backend servers</td>
<td>Managed services provides full testing capabilities</td>
</tr>
<tr>
<td>Automotive compliance (MISRA, ISO26262)</td>
<td>Supply chain total health with SW BoM</td>
<td>Fuzzing for automotive protocols and 5G</td>
<td>Architecture/design modeling eliminates software flaws</td>
</tr>
</tbody>
</table>
## Synopsys Polaris Software Integrity Platform™

### Polaris

- **Centralized Management**
- **Consolidated Reporting**
- **Alerting & Workflow**

### Integrated Analysis Engines

<table>
<thead>
<tr>
<th>SAST</th>
<th>SCA</th>
<th>DAST/IAST</th>
<th>Managed Services</th>
</tr>
</thead>
<tbody>
<tr>
<td>Coverity</td>
<td>Black Duck</td>
<td>Seeker</td>
<td>SAST/DAST, Pen, Network</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Tinfoil Defensics</td>
<td></td>
</tr>
</tbody>
</table>

- **CI/CD Integration**
- **Cloud/On-Prem Deployment**

**Platform that integrates products and managed services**
- Cloud-based
- Enterprise scale
- Single user interface, easy to use
- Unified management reports
- Includes key DevOps feature: plug-in to enable developers to find and fix vulnerabilities directly from desktop
Recognized Leader in End-to-End Application Security
Synopsys is the only vendor recognized as the leader in both SAST and Open Source (SCA)

Gartner Magic Quadrant Application Security Testing

Download a free copy of the 2019 Magic Quadrant for Application Security Testing.
Recognized Leader in End-to-End Application Security

Synopsys is the only vendor recognized as the leader in both SAST and Open Source (SCA)
Our mission is to help you build secure, high-quality software faster for years to come.
Growth Strategy

- High-value products and consulting services to enable efficient and effective security testing during the software development process
  
  - **Expand Polaris Software Integrity Platform™**
    
    Cloud-based platform integrating multiple technologies: expands testing capabilities and supports large-scale deployments; roadmap includes additional integrations throughout 2020
  
  - **Continue to Scale Field**
    
    Consulting engagements: High-level benchmarking, program development guidance and large product deployments
    
    Sales channel: leverage international presence, continually enhance capacity and focus on large enterprise customers, key verticals and new regional business
  
  - Balance near-term growth and profitability with investments to capitalize on long-term opportunity
Uniquely Positioned for Stability and Growth

- Electronics complexity driving growth
- Clear technology leadership
- Financial strength and growth
Excellent Financial Execution

~90% recurring revenue model with $4.4B noncancelable backlog provides stability throughout the business cycle***

* FY15 to FY20E GAAP to non-GAAP EPS reconciliation is included at the end of this presentation
** FY20E reflects guidance provided as of February 19, 2020
*** We define recurring revenue as revenue generated from time-based products, multi-period IP contracts, and maintenance and services, for which revenue is recognized over time
Cash Flow & Capital Allocation

Roughly tracks EBITDA less cash taxes on multi-year basis

Variable from year to year, based mainly on timing of large contracts and payments

* Includes approximately $230M in one-time payments
** FY20E reflects guidance provided on February 19, 2020

Operating Cash Flow (M)

- FY15: $495
- FY16: $587
- FY17: $635
- FY18*: $424
- FY19: $801
- FY20E**: $800 - $825

Stock Repurchases (M)

- FY15: $280
- FY16: $400
- FY17: $400
- FY18: $400
- FY19: $329
- Q1’20: $100

Repurchased ~$1.9B in stock since 2015, reducing share count by ~4M shares

~80% of FCF returned to investors via share buybacks since 2015

Capital allocation priorities:
- Internal investments
- Buybacks and acquisitions
Market & Technology Leader:
• Electronic Design Automation
• Semiconductor Intellectual Property
• Software Security & Quality

Solid Financials:
• Recurring revenue model
• Large, non-cancellable backlog
• Excellent execution
GAAP to Non-GAAP Reconciliation
Earnings Per Share – Full Fiscal Year 2020 Targets

<table>
<thead>
<tr>
<th></th>
<th>Range for Fiscal Year</th>
<th>October 31, 2020</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Low</td>
<td>High</td>
</tr>
<tr>
<td>Target GAAP earnings per share</td>
<td>$3.68</td>
<td>$3.87</td>
</tr>
<tr>
<td>Adjustments:</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Estimated impact of amortization of intangible assets</td>
<td>0.59</td>
<td>0.56</td>
</tr>
<tr>
<td>Estimated impact of stock compensation</td>
<td>1.47</td>
<td>1.44</td>
</tr>
<tr>
<td>Acquisition-related costs</td>
<td>0.02</td>
<td>0.02</td>
</tr>
<tr>
<td>Estimated impact of restructuring charges</td>
<td>0.12</td>
<td>0.06</td>
</tr>
<tr>
<td>Impact of tax adjustments</td>
<td>(0.70)</td>
<td>(0.70)</td>
</tr>
<tr>
<td>Target non-GAAP earnings per share</td>
<td>$5.18</td>
<td>$5.25</td>
</tr>
</tbody>
</table>

Shares used in non-GAAP calculation (midpoint of target range) 154,500 154,500

For additional information about the measures Synopsys uses to evaluate its core business operations, please review the earnings press release and Item 2.02 of the Current Report on Form 8-K filed on February 19, 2020.
## GAAP to Non-GAAP Reconciliation

### Operating Margin – Full Fiscal Year 2019 and Midpoint of 2020 Targets

<table>
<thead>
<tr>
<th></th>
<th>Twelve Months Ended October 31, 2019</th>
<th>Twelve Months Ending October 31, 2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>GAAP operating margin</td>
<td>15.5%</td>
<td>17.9%</td>
</tr>
<tr>
<td>Amortization of intangible assets</td>
<td>3.0%</td>
<td>2.4%</td>
</tr>
<tr>
<td>Stock compensation</td>
<td>4.6%</td>
<td>6.2%</td>
</tr>
<tr>
<td>Acquisition-related costs</td>
<td>0.2%</td>
<td>0.1%</td>
</tr>
<tr>
<td>Restructuring charges</td>
<td>1.4%</td>
<td>0.4%</td>
</tr>
<tr>
<td>Legal matters</td>
<td>-0.5%</td>
<td>0.0%</td>
</tr>
<tr>
<td>Change in value of non-qualified deferred compensation</td>
<td>0.8%</td>
<td>0.0%</td>
</tr>
<tr>
<td><strong>Non-GAAP operating margin</strong></td>
<td><strong>25.0%</strong></td>
<td><strong>27.0%</strong></td>
</tr>
</tbody>
</table>

(1) Synopsys' fiscal year 2019 ended on November 2, 2019, and its fiscal 2020 will end on October 31, 2020. For presentation purposes, we refer to the closest calendar month end.

(2) These numbers represent the midpoint of targets in the prepared remarks provided on February 19, 2020, and do not represent official guidance for fiscal 2020.

For additional information about the measures Synopsys uses to evaluate its core business operations, please review the earnings press release and Item 2.02 of the Current Report on Form 8-K filed on February 19, 2020.
GAAP to Non-GAAP Reconciliation

Earnings Per Share – Full Fiscal Years 2015 to 2019 Actual Results

<table>
<thead>
<tr>
<th></th>
<th>ASC 605 FY 2015</th>
<th>ASC 605 FY 2016</th>
<th>ASC 605 FY 2017</th>
<th>ASC 605 FY 2018</th>
<th>ASC 605 FY 2019</th>
<th>ASC 606 FY 2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>GAAP diluted net income per share</td>
<td>$1.43</td>
<td>$1.73</td>
<td>$0.88</td>
<td>$2.82</td>
<td>$3.45</td>
<td></td>
</tr>
<tr>
<td>Adjustments:</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Amortization of intangible assets</td>
<td>0.86</td>
<td>0.84</td>
<td>0.70</td>
<td>0.82</td>
<td>0.65</td>
<td></td>
</tr>
<tr>
<td>Stock compensation</td>
<td>0.54</td>
<td>0.63</td>
<td>0.70</td>
<td>0.91</td>
<td>1.01</td>
<td></td>
</tr>
<tr>
<td>Acquisition-related costs</td>
<td>0.10</td>
<td>0.07</td>
<td>0.06</td>
<td>0.14</td>
<td>0.04</td>
<td></td>
</tr>
<tr>
<td>Restructuring charges</td>
<td>0.10</td>
<td>0.06</td>
<td>0.24</td>
<td>0.08</td>
<td>0.31</td>
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<tr>
<td>Legal and tax matters</td>
<td>(0.04)</td>
<td>-</td>
<td>0.05</td>
<td>0.17</td>
<td>(0.12)</td>
<td></td>
</tr>
<tr>
<td>Income tax related to transition tax</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>0.41</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>Income tax related to tax rate change</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>0.33</td>
<td>-</td>
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</tr>
<tr>
<td>Income tax related to tax restructuring</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>(1.12)</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>Tax Impact of repatiation</td>
<td>-</td>
<td>-</td>
<td>1.07</td>
<td>-</td>
<td>-</td>
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</tr>
<tr>
<td>Tax settlement</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>0.11</td>
<td></td>
</tr>
<tr>
<td>Tax adjustments</td>
<td>(0.22)</td>
<td>(0.31)</td>
<td>(0.28)</td>
<td>(0.65)</td>
<td>(0.89)</td>
<td></td>
</tr>
</tbody>
</table>

Non-GAAP diluted net income per share $2.77 $3.02 $3.42 $3.91 $4.56

For additional information about the measures Synopsys uses to evaluate its core business operations, please review the earnings press release and Item 2.02 of the Current Report on Form 8-K filed on February 19, 2020.
Thank You