Synopsys, Inc. (Nasdaq: SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As an S&P 500 company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP and offers the industry’s broadest portfolio of application security testing tools and services. Whether you’re a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing more secure, high-quality code, Synopsys has the solutions needed to deliver innovative products. Learn more at www.synopsys.com.

### Synopsys Overview and History

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<table>
<thead>
<tr>
<th>Company Founded:</th>
<th>1986</th>
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<tbody>
<tr>
<td>Headquarters:</td>
<td>690 East Middlefield Rd. Mountain View, CA 94043</td>
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<tr>
<td>Revenue Q3 FY 2021:</td>
<td>$1,057 million*</td>
</tr>
<tr>
<td>Revenue for FY 2020:</td>
<td>$3.69 billion**</td>
</tr>
<tr>
<td>Employees:</td>
<td>~15,800***</td>
</tr>
<tr>
<td>Locations:</td>
<td>122 offices worldwide in North America, Europe, Japan, the Pacific Rim, India, and Israel</td>
</tr>
<tr>
<td>Contact:</td>
<td>(650) 584-5000 <a href="http://www.synopsys.com">www.synopsys.com</a></td>
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* 9.6% growth over third quarter of Fiscal Year 2020
** 9.8% growth over Fiscal Year 2019
*** Total employees worldwide as reported for third quarter Fiscal Year 2021
Financial Information

Synopsys stock has traded on the Nasdaq Stock Exchange under the symbol "SNPS" since the company's initial public offering in 1992. Recent fiscal results are as follows:

- Revenue Q3 Fiscal Year 2021: $1,057 million; 9.6% growth over third quarter of Fiscal Year 2020
- Revenue Fiscal Year 2020: $3.69 billion; 9.6% growth over Fiscal Year 2019
- Estimated revenue for Fiscal Year 2021: $4.05 billion

Strategic Acquisitions

**SOFTWARE**
- 2017 Black Duck
- 2016 Cigital
- 2015 Codenomicon
- 2014 Coverity

**VERIFICATION & PROTOTYPING**
- 2015 Atrenta
- 2012 EVE
- 2012 SpringSoft
- 2008 Synplicity
- 1997 Viewlogic
- 1994 Logic Modeling

**IP**
- 2010 Virage Logic
- 2002 inSilicon

**DESIGN**
- 2012 Magma
- 2002 Avanti
- 1997 EPIC

**SILICON ENGINEERING**
- 2012 Mask synthesis technology *from Luminescent Technologies
- 2010 Optical Research
- 2003 Numerical Technologies
Management Team

The company’s executive management team is comprised of leaders with diverse global backgrounds and many decades of combined semiconductor industry experience. The tremendous “know how” they bring to Synopsys has helped the company become its industry leader.

Industry-Leading Solutions, from Silicon to Software

Synopsys technology is at the heart of innovations that are changing the way we work and play. Autonomous vehicles. Artificial intelligence. The cloud. 5G. These breakthroughs are ushering in the era of Smart Everything—where devices are getting smarter, everything’s connected, and security is critical.

Powering this new era of digital innovation are advanced silicon chips and exponentially growing amounts of software content—all working together, smartly and securely. Synopsys is at the forefront of Smart Everything with the world’s most advanced technologies for chip design and software security and quality testing. We help our customers innovate from silicon to software so they can deliver Smart Everything.

Build the Best Chips, Faster

Synopsys is the world’s leading provider of solutions for designing and verifying advanced silicon chips, and for designing the next-generation processes and models required to manufacture those chips. Our design technologies maximize productivity and ensure the best quality of results while optimizing chips for power, performance, and cost. Our verification technologies cut months off project schedules by enabling the verification of complex chips together with software, faster. We help customers find system-level bugs earlier and faster, bring-up software sooner, and validate their entire system.
Integrate More Capabilities, Faster

Synopsys offers the world’s broadest portfolio of silicon IP—pre-designed blocks of logic, memory, interfaces, analog, security, and embedded processors—to help our customers introduce more capabilities and reduce integration risk. Our interface IP supports the most popular protocols like USB and HDMI. Our processor IP solutions include CPUs and DSP cores, subsystems, and development tools that enable customers to meet the performance and power requirements of their systems-on-chip. Our security IP such as hardware root of trust and cryptography IP helps prevent evolving threats in connected devices. And our IP Accelerated Initiative provides architecture design support, IP subsystems, signal/power integrity analysis, and more so customers can integrate IP that’s tuned to their designs.

Build Secure Software, Faster

Synopsys helps customers build security and quality into the DNA of their software code—at any stage of the software development lifecycle and across the supply chain—to minimize risks while maximizing speed of application development. Our static analysis, software composition analysis, and dynamic analysis solutions allow customers to find and fix vulnerabilities and defects in proprietary code, open source components, and application behavior with unmatched depth, accuracy, and speed. We help companies test their software early and often, so they can avoid costly product crashes, security breaches, and catastrophic system failures. We also automate the process of securing and managing open source software to ensure license compliance, remediate vulnerabilities, and reduce operational risks.

Professional Services

Synopsys’ services organization provides consulting and design services to help chip developers accelerate innovation and achieve success in their design programs. These services are tightly aligned with our EDA, IP, FPGA and optical products to advance customers’ learning curves and help them develop and deploy advanced methodologies that improve their productivity and design results. Our worldwide team of design consultants complements our customers’ core competencies to mitigate their project risks and accelerate the implementation of their chips.

Strategic Alliances

Synopsys has long-established and proven strategic alliances with industry leaders dedicated to jointly solving the challenges of system complexity and nanometer scale for leading-edge SoC design.


Design Service Partners: Alchip, ANSYS, eSilicon, GUC, Open-Silicon, and VeriSilicon.

FPGA Partners: Achronix, Intel, Lattice, Microsemi, QuickLogic, and Xilinx.

Foundries: ams, Dongbu HiTek, Mie Fujitsu Semiconductor, GLOBALFOUNDRIES, Hua Hong Semiconductor, Intel Custom Foundry, MagnaChip, Samsung Foundry, SiTerra, SMIC, TowerJazz, TSMC, UMC, and X-FAB.

Library and IP Partners: Arm, CEVA, Faraday, PLDA, and VeriSilicon.

Other Solution Partners: Silicon Catalyst.

Synopsys Users Group (SNUG)

Since 1991, the Synopsys Users Group (SNUG) has represented a global design community focused on innovating from silicon to software. Today, as the electronics industry’s largest user conference, SNUG brings together nearly 10,000 Synopsys tool and technology users across North America, Europe, Asia, and Japan. In addition to peer-reviewed technical papers and insightful keynotes from industry leaders, SNUG provides a unique opportunity to connect with Synopsys executives, design ecosystem partners, and members of the local design community.
Synopsys Community Affairs

Synopsys is committed to strengthening the communities where we live and work. We create opportunities for employees worldwide to put their passion for giving and volunteering into action, empowering people to build more vibrant communities.

Through the year-round Synopsys Shares program, we volunteer, collect essential items through donations drives, raise funds for causes and charities, and help communities recover and rebuild in times of disaster. We encourage and facilitate employee giving to the causes our people care about and help them connect with charitable organizations worldwide. In 2020, we contributed nearly 5,900 volunteer hours and supported 175 community projects to benefit 131 partners.

Each year, Synopsys commits a percentage of revenue to charitable giving through The Synopsys Foundation and corporate donations. In 2020, $3.6 million in philanthropic investments focused on Science, Technology, Engineering, and Mathematics (STEM) education programs that create access and opportunity for primary and secondary students in underserved areas and support for critical community needs. Through the Synopsys entities in India, our donations comply with Corporate Social Responsibility spending requirements and continue the focus on education and science learning. In 2019, we celebrated the 20th anniversary of the Synopsys Silicon Valley Science and Technology Outreach Foundation, which has supported 2.2+ million science projects since its founding.