

Stelios Diamantidis

Director, AI Products, Design Group



Stelios leads AI vertical strategy for the Synopsys Design Group and is a founder and director of the Machine Learning Center of Excellence, where he looks at applying machine learning (ML) technology to key disruptions in the design and manufacturing of integrated computational systems. He has more than 20 years of experience in system and semiconductor design and EDA software.

Stelios started his career at global high-performance computing and visualization leader Silicon Graphics, where he held technical and engineering management positions for the iconic InfiniteReality 3D graphics supercomputer. He then went on to found Globetech Solutions and Tesla Design Automation, two innovative software companies addressing the combined space of architectural exploration, design verification, and test of semiconductors. As CEO and CTO, respectively, Stelios grew revenue from concept to multi-million dollar operations worldwide with marquee customers such as Intel, Samsung, STMicroelectronics, and Texas Instruments.

A passionate technologist, Stelios has pioneered several EDA applications including design verification IP (2001), constrained-random metric-driven verification (2003), DFT verification (2005), and at-speed post-silicon validation (2007). He has also driven the definition of IEEE standards including IEEE1647 (vice chair, Standard for Functional Verification Language) and IEEE1687 (founding member, Standard for Access and Control of Instrumentation). Stelios has published numerous papers and articles on the design and test of semiconductors and routinely presents at industry conferences and events.

Since joining Synopsys in 2010, Stelios has led product strategy and management of software product lines for the physical design and verification of advanced integrated circuits. In 2014, Stelios received the Synopsys Excellence award for his work on the introduction of IC Compiler II, the industry's leading design platform used in thousands of production tapeouts worldwide at advanced process nodes. In 2017, he co-founded the Machine Learning Center of Excellence, a team focused on enabling artificial intelligence in chip design.

Stelios holds a Master of Science degree in Electrical Engineering from Stanford University.