

Stelios Diamantidis
Senior Director, AI Strategy & Products



Stelios Diamantidis leads strategy and product management for Synopsys AI, and is a founder of the Machine Learning Center of Excellence, where he looks at applying machine learning (ML) technology to key disruptions in the design and manufacturing of integrated computational systems. He has more than 20 years of experience in system and semiconductor design and EDA software. A passionate technologist, Stelios has pioneered several EDA applications including design verification IP (2001), constrained-random metric-driven verification (2003), design-for-test (DFT) verification (2005), and at-speed post-silicon validation (2007). He has also driven the definition of IEEE standards including IEEE1647 (vice chair, Standard for Functional Verification Language) and IEEE1687 (founding member, Standard for Access and Control of Instrumentation). Stelios has published numerous papers and articles on the design and test of semiconductors and routinely presents at industry conferences and events. He holds an M.S. degree in electrical engineering from Stanford University.