

## Siva K. Yerramilli

Corporate Vice President of Strategic Programs, Silicon Engineering Group, Synopsys



Siva K. Yerramilli is Corporate VP of Strategic Programs in the Silicon Engineering Group at Synopsys. In this role, he is responsible for driving cross-business unit initiatives including Design Technology Co-Optimization (DTCO), which is aimed at achieving better results (performance, power, and area) in the face of ever-increasing complexity by utilizing Synopsys' capabilities from silicon to systems. Siva is also responsible for the photonics IC development business, which is poised well to power the high-speed I/O roadmap. Siva joined the company in 2019.

Prior to Synopsys, Siva held several technical and management positions across various aspects of technology enablement and silicon product development at Intel. He has more than 30 years of experience in the tech industry.

Siva received his Bachelor's degree in electronics and telecommunications from Jawaharlal Nehru Technological University, India. He also studied computer science at the Indian Statistical Institute in Kolkata, India. Siva received his Master's in computer engineering at Syracuse University and later completed the Executive Management Program at Harvard University.