## Shankar Krishnamoorthy

**Senior Vice President, Digital Implementation Solutions**

Shankar rejoined Synopsys in August 2017 as senior vice president of Digital Implementation Solutions. He and his organization are responsible for Synopsys' synthesis and place-and-route solutions, including IC Compiler II and Design Compiler.

Shankar has more than 25 years of experience leading world-class teams that have delivered the industry's top IC physical design and logic synthesis solutions. He joins us from Mentor Graphics where he was general manager of the IC Design Solutions Division, responsible for their analog and digital design implementation platforms. He joined Mentor with the 2007 acquisition of Sierra Design Automation where he was Founder and CTO.

Prior to Sierra Design, Shankar led Synopsys' Physical Synthesis and Logic Synthesis R&D organizations. He began his career at Synopsys in 1992 working on technology mapping in Design Compiler.

Shankar received his M.S. in Computer Science from the University of Texas, Austin and his B. Tech in Computer Science from the Indian Institute of Technology, Bombay.