

Shankar Krishnamoorthy

General Manager Digital Design Group, Corporate Staff



Shankar is the general manager of the Digital Design Group, responsible for the digital design platform including synthesis, signoff analysis, place-and-route, test automation, and formal verification solutions. Most recently, he served as senior vice president of the Digital Implementation Group, delivering several game-changing innovations including Fusion Compiler, RTL Architect, 3DIC Compiler, TestMAX solutions. Shankar has more than 25 years of experience leading world-class teams that have delivered the industry's premier IC physical design and logic synthesis solutions.

Before rejoining Synopsys in 2017, he was at Mentor Graphics where he served as general manager of the IC Design Solutions Division. He joined Mentor in 2007 as part of the acquisition of Sierra Design Automation, where he was the founder and CTO. Prior to Sierra Design, Shankar led Synopsys' Physical Synthesis and Logic Synthesis R&D organizations. He began his career at Synopsys in 1992 working on logic synthesis technology.

Shankar received his M.S. in Computer Science from the University of Texas, Austin in 1992, and his bachelor's degree in Technology and Computer Science from the Indian Institute of Technology, Bombay in 1990.