

Shankar Krishnamoorthy

General Manager, Silicon Realization Group



Shankar Krishnamoorthy is the General Manager of the Silicon Realization Group, leading the team behind the design and verification software platforms from Synopsys. His group innovates across a broad spectrum of EDA technologies including simulation, static analysis, debug, synthesis, signoff analysis, place-and-route, test automation, and formal verification solutions. Prior to this role, he was General Manager of the Digital Design Group, with responsibility for the Synopsys digital design platform. In earlier roles, he served as Senior Vice President of the Digital Implementation Group, delivering several game-changing innovations including Fusion Compiler™, RTL Architect, 3DIC Compiler, and TestMAX solutions. Before rejoining Synopsys in 2017, Shankar was at Mentor Graphics, where he served as General Manager of the IC Design Solutions Division. He joined Mentor in 2007 with the acquisition of Sierra Design Automation, where he was Founder and CTO. Prior to Sierra Design, Shankar led

Synopsys' physical synthesis and logic synthesis R&D organizations. Shankar received his M.S. in Computer Science from the University of Texas, Austin, and his bachelor's degree in Computer Science from the Indian Institute of Technology, Bombay.