

Ravi Subramanian

GM, Systems Design Group



Ravi Subramanian is general manager of the Systems Design Group and a member of the Corporate Executive Staff at Synopsys. In this role, he is responsible for the business unit, developing comprehensive system, IC, and software verification and validation solutions for systems and semiconductor companies across key vertical markets.

Subramanian brings more than 25 years of experience in semiconductor design, wireless communications, and electronic design industries as a business leader, technologist, and entrepreneur. Before joining Synopsys in August 2022, he was Senior VP & GM of IC Verification at Siemens EDA, responsible for IC verification products and solutions spanning analog-mixed signal (AMS)/radio frequency (RF)/digital verification, emulation and prototyping, virtual SoC platforms, and electronic digital twins. He began his career at AT&T Bell Laboratories, where he won the AT&T Leadership Award for his work on the world's first merchant Global System for Mobile Communications (GSM) chipset. After five years at Bell Labs, Subramanian then founded and led two successful Silicon Valley startups: MorphICs Technologies (a 3G fabless semiconductor company acquired by Infineon) and Berkeley Design Automation (an analog/RF EDA company acquired by Mentor – now Siemens EDA). BDA was recognized for multiple years in a row as one of the fastest growing technology companies in North America by revenue growth.

Subramanian holds a Ph.D. degree in EECS from the University of California, Berkeley, where he was a recipient of the prestigious UC Regent's Fellowship. He received his B.Sc. degree in EE (with honors) from the California Institute of Technology.