

Raja Tabet

Senior Vice President, Engineering, Custom Design Manufacturing Group



Raja Tabet is Senior Vice President, Engineering, for the Custom Design Manufacturing Group at Synopsys. He and his team are responsible for creating differentiated, innovative technologies in custom design, circuit simulation, physical verification, TCAD, and mask solutions.

Tabet has more than 25 years of experience as a business and technical leader in the semiconductor industry, particularly in the networking, data center, and automotive market segments. Prior to Synopsys, Tabet was at Cadence serving as Corporate Vice President of Emerging Technologies, working on broad-based solutions for various market segments.

He also served for 10 years as Freescale's Vice President of Software Solutions, creating solution platforms for Freescale's product lines and launching a successful and rapidly growing and thriving software and services business. Prior to Freescale, Tabet was at IBM, and served in a multitude of technical and leadership roles in the company's Microelectronics Division. He has a master's degree in Computer Engineering from Syracuse University and a B.S. degree in Computer Science from SUNY Plattsburg.