

Raja Tabet

Senior Vice President, Custom Design and Physical Verification Group



Raja Tabet is Senior Vice President of the Custom Design and Physical Verification Group at Synopsys. He and his team are responsible for creating differentiated, innovative technologies for the custom circuit design community.

Raja has more than 25 years of experience as a business and technical leader in the semiconductor industry, particularly in the networking, data center, and automotive market segments. Prior to Synopsys, Raja was at Cadence, serving as Corporate Vice President of Emerging Technologies and working on broad-based solutions for various market segments.

He also served for 10 years as Vice President of Software Solutions at Freescale, creating solution platforms for Freescale's product lines and launching a successful and rapidly growing and thriving software and services business. Prior to Freescale, Raja was at IBM, serving in multiple technical and leadership roles in the company's Microelectronics Division.

Raja has a master's degree in computer engineering from Syracuse University and a bachelor of science degree in computer science from SUNY Plattsburg.