

Ming Zhang
Distinguished Architect



Ming Zhang is a Distinguished Architect at Synopsys, leading corporate strategy for 3DIC technology. Prior to Synopsys, he was a circuit designer at Intel specializing in low-power and fault-tolerant designs, foundry engineer at Samsung specializing in DFX, software developer at AI startups working on algorithm development and cloud deployment, and, most recently, the co-founder and CEO of a chiplet startup, zGlue.

Dr. Zhang holds a Ph.D. EE in VLSI from the University of Illinois at Urbana-Champaign (UIUC), an M.S. in MEMS from UIUC, and a B.S. in Physics from Peking University in China.