Johannes Stahl

Director, Product Marketing, Prototyping and FPGA

Dr. Johannes Stahl is responsible for physical and virtual prototyping tools, as well as FPGA design tools at Synopsys. He has over 20 years of experience including technical and business responsibilities for tools and services for SoC design and software development. During his earlier tenure at Synopsys, Dr. Stahl was responsible for their SystemC products as well as driving the rollout of the SystemC initiative. He has lead a Synopsys wireless engineering services team, that delivered custom IP cores for wireless and broadcasting applications to major semiconductor companies that also pioneered and successfully executed methodologies for first time silicon success. Before Synopsys, Dr. Stahl had marketing responsibility in the executive team at CoWare, where he was managing major product lines as well as all IP partner relationships. Dr. Stahl holds Dipl.-Ing. and Dr.-Ing. Degrees in Electrical Engineering from Aachen Technical University, Germany.