## **Christopher Tice**

## **VP of Engineering, Systems Design Group**



Christopher Tice is VP of Engineering in the Systems Design Group, responsible for Synopsys Hardware Marketing and Manufacturing. He is also responsible for driving the growth of Synopsys' verification business based on a foundation of systems tools and solutions addressing fast-growing verticals such as aerospace & defense, networking, and 5G. Before joining Synopsys, he was an entrepreneur-in-residence at SK Telecom Innopartners. Christopher spent 16 years at Cadence Design Systems, where he was Senior Vice President and General Manager of the Hardware and Systems Groups. Prior roles included Vice President of Quickturn Design Systems and General Manager of Weitek's Processor Business Group. He received his BSEE with honors from the University of Florida and attended the MBA School at Florida Atlantic University. He was elected as

an inaugural member of the Electrical and Computer Engineering Academy of the University of Florida in 2009.