

Andrew Dauman

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Andrew Dauman is Vice President of Engineering for Synopsys' Verification Group and oversees the development of FPGA products that include the Synplify family of FPGA synthesis tools and the HAPS FPGA-based prototyping systems.

Previously, Mr. Dauman was Vice President of Engineering for Synplicity, Inc., where he first became involved in FPGAs in 1994. In his early career, he held a variety of positions in semi-custom processor design and software development for radar systems and logic synthesis.

Mr. Dauman holds a B.S.E.E. degree from Boston University.