

SNUG Israel 2018

Call for Papers FAQ

What is SNUG (Synopsys Users Group)?

SNUG began as a single user conference in 1990 in Silicon Valley. Today, it's a global program with 13 conferences held in ten countries attended by more than 10,000 users. As SNUG expands to address the complexities of the entire 'Silicon to Software' ecosystem, we continue in our tradition of co-creating a high-quality program that is centered on providing you with hands-on information and insights into today's toughest challenges. SNUG also provides a fantastic opportunity to learn from and network with your gifted peers and Synopsys experts who are eager to learn about your challenges and collaborate with you to solve them.

What do past participants say about SNUG?

"I learned more on the first day alone than I had previously learned reading materials and doing web research."

"Like-minded engineers. . . papers providing good solutions."

"Great forum to get answers and meet/talk to technical experts from Synopsys on their tools and flows."

I've never submitted a paper for SNUG. What is involved?

SNUG has a proven process and a dedicated technical committee who will work with you to create and deliver your real-world experience paper that reflects your innovation using Synopsys tools. The process and deadlines are outlined on the [SNUG Israel Author Kit](#).

When are abstracts due?

The abstract submission deadline is October 25, 2018.

When will I know if my paper proposal has been selected?

The SNUG Technical Committee will review the submitted proposals and notify authors whether their proposal has been accepted by December 4, 2018. Once your proposal is accepted, the paper writing process begins.

Do you have some topics for me to think about?

Here is a list to get you started – please do not limit your ideas!

- Analog Mixed Signal Simulation
- Custom Implementation
- Design Turnaround Time and Performance Optimization
- FPGA Synthesis
- IP Integration into SoCs
- Low Power Design and Analysis
- Silicon Test and Yield Analysis
- SoC Verification
- Software Bring-up
- Timing Analysis and Sign-off

For more information, please visit the SNUG [Call for Papers](#) or [Author's Kit](#).

Ready to submit your abstract? [Get started now](#).