



August 30, 2006

5F Yusen Shin Yokohama Bldg.,  
17-2 Shin Yokohama 3-chome,  
Kohoku-ku, Yokohama, 222-0033, Japan

Dear Synopsys;

Congratulations on 20 years of successful business!

The semiconductor design industry needs good EDA tools for good designs. Synopsys provides us the best-in-class tools such as logic synthesis, verification, place and routing, DFT, TCAD and DFM in 20 years. We can design the most complicated system LSI at each process technology by using the excellent tools.

And also we require good technical supports for actual designs besides the EDA tools. Synopsys is continuously supporting us a lot. Therefore we can make good progress in the system LSI designs.

The semiconductor industry is walking along with new technology. Synopsys is aggressively developing new technology to meet our industry demand. As a result, both EDA and semiconductor industries produce win-win situation. I hope that the strategy of Synopsys will always match to our strategy in future as well as in the past. And we would like to collaborate more closely and tightly with Synopsys to lead the design environment in system LSI to enjoy the fruitful results together.

Happy 20th Anniversary, Synopsys!

A handwritten signature in black ink, appearing to read "Nobuyuki Nishiguchi".

Nobuyuki Nishiguchi

Vice President and General Manager , Development Department 1  
STARC (Semiconductor Technology Academic Research Center)