



ELECTRONICS

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Dear Synopsys,

Samsung is pleased to congratulate Synopsys for its accomplishments as it reaches its twentieth anniversary.

Samsung is proud to be regarded as one of the world's leading companies, providing products that are highly respected in the marketplace. Samsung has moved to the forefront of our industry because of our dedication to the principles of change, innovation, and creativity. As I recall the long relationship between Samsung and Synopsys, I note that Synopsys has employed similar principles. Examples that come to mind include:

- Synopsys' development and refinement of fundamental logic synthesis technology transitioned the electronics industry from traditional schematic capture-based design to HDL-based designs, resulting in a quantum leap in design productivity.
- Practical application of static timing analysis as a required part of the design sign-off process represented a major improvement over simulation-based timing analysis, significantly shortening design time and improving quality.
- Creation of a complete RTL-to-GDSII design environment by combining logic synthesis and static timing analysis capabilities with floorplanning, physical synthesis, placement and routing, parasitic extraction and physical verification has enabled numerous business and technical efficiencies.
- Additional advances in functional verification, DFT, analog/mixed signal simulation, TCAD, OPC, DFM and IP have allowed Synopsys to offer a complete solution that addresses all major aspects of the design process.

The application of these principles has direct benefits to our customers as they tackle today's design challenges. Samsung, along with its Common Platform Technology partners IBM and Chartered Semiconductor Manufacturing, recently announced the availability of a complete 65-nanometer Synopsys design flow. This design flow allows designers to address area, performance, signal integrity, power and DFM goals in a complete and consistent environment. This means that our customers can utilize Samsung's leading-edge process technologies with confidence in achieving "right the first time" silicon.

We look forward to extending the Samsung-Synopsys relationship as we move to 45 nanometers and beyond.

Happy 20th Anniversary, Synopsys!

Jeong-Taek Kong, Ph.D.

Vice President, Corporate Computer-Aided Engineering