

August 15, 2006

Dear Synopsys,

Warmest wishes and congratulations from Chartered Semiconductor Manufacturing on your 20th anniversary!

In our many years of working together, Chartered and Synopsys have demonstrated a shared commitment to enabling our customers with solutions that reduce risk and speed time to market, as well as offer customers choice and flexibility.

As one of the world's top dedicated semiconductor foundries with a range of mature and leading-edge process technologies, we have been pleased to partner with you on a breadth of solutions, including reference flows and IP that support the needs of our customers at various nodes and in various application segments.

Most recently, Chartered and our joint development partner, IBM, were among the first companies at 65nm to have you port your reduced-risk IP. This was an exciting development not just for us, but our customers. These high-value cores from Synopsys allow chip designers to quickly obtain and integrate critical functionality into their designs and then ramp into volume production at Chartered or IBM.

In addition, we have now made available the Synopsys 65nm reference flow for Common Platform technology, which will address the need to continuously strive for higher yields while lowering costs. The complete and consistent flow addresses designers' needs for quickly meeting area, performance, power, signal integrity, and DFM goals. In working with you to validate the various data models and flows and demonstrate the flow's quality-of-results and ease-of-use, we are pleased that our customers can come to Chartered with confidence in achieving first-time silicon success at 65nm as well as enjoying flexibility in their sourcing strategies.

As we move ahead to 45nm and beyond, we look forward to continuing to build on our partnership.

Yours sincerely,



L.C. Hsia
Senior Vice President
Technology Development