Key Customers Partner to Drive IC Compiler II – the Power of 10X

“Imagination has long been an early adopter of advanced design planning technology that delivers tangible results. Thanks to some unique design planning capabilities offered by IC Compiler II, we observed a boost to the throughput of our design exploration flow by at least 10X. As a result, we were able to realize a high quality floorplan for one of our latest PowerVR Wizard GPU cores in a fraction of the time we’d normally assign for this task.”

– Mark Dunn
EVP IMGworks SoC Engineering
Imagination Technologies

“Our results with IC Compiler II are very promising. Initial applications have demonstrated impressive turn-around times, especially at early phases where design exploration hinges on speed-to-results. While using IC Compiler II on a large hierarchical module of a current production design, we also noted significant improvements in QoR. We look forward to continued collaboration with Synopsys on the use of IC Compiler II.”

– JC Parker
Sr. Director - Design Tools & Methodologies
LSI Corporation

“IC Compiler II delivers breakthrough performance compared with IC Compiler, accelerating design turnaround, reducing memory requirements. We were particularly impressed by its scalable MCMM technology, which considers operating scenarios. We have already deployed IC Compiler II in real use and have leveraged the runtime power to improve the performance and reliability of our production LSI design.”

– Yasui Takuya
Manager, System LSI Business Division
Panasonic

Quotes continued on next page
“We have a long history of collaboration with Synopsys on improving throughput and productivity in physical design. IC Compiler II represented the single biggest leap in productivity we have seen in many years. On a Renesas design, IC Compiler II exhibited at least 7X runtime and 3X memory improvement, while delivering competitive quality of results. We are working with Synopsys to deploy IC Compiler II into production use.”

— Tatsuji Kagatani
Department Manager of Design Automation Department
System Integration Business Division
Renesas Electronics Corporation

High-performance design is Samsung’s lifeblood and Samsung sees IC Compiler II as key enabler for pushing the envelope on performance. IC Compiler II has demonstrated exceptional implementation speed-up on Samsung’s key processor design and Samsung is actively collaborating with Synopsys on utilizing the throughput gains to drive higher performance in production designs.

Samsung

“In our ongoing tapeout with IC Compiler II, STMicroelectronics is benefiting from an unprecedented 10X faster floorplan creation and 5X faster design implementation over previous approaches. Coupled with the capacity to handle partitions 2-3 times larger than any other solution, it is paving the way to an entire new way of thinking about physical design.”

— Indavong Vongsavady
Director, Design Enablement and Services, Central CAD & Design Solutions
STMicroelectronics